

FIG. 1  
PRIOR ART

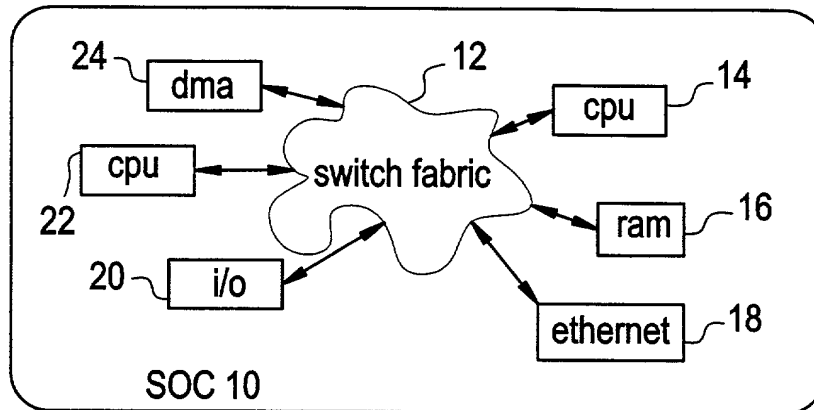


FIG. 2

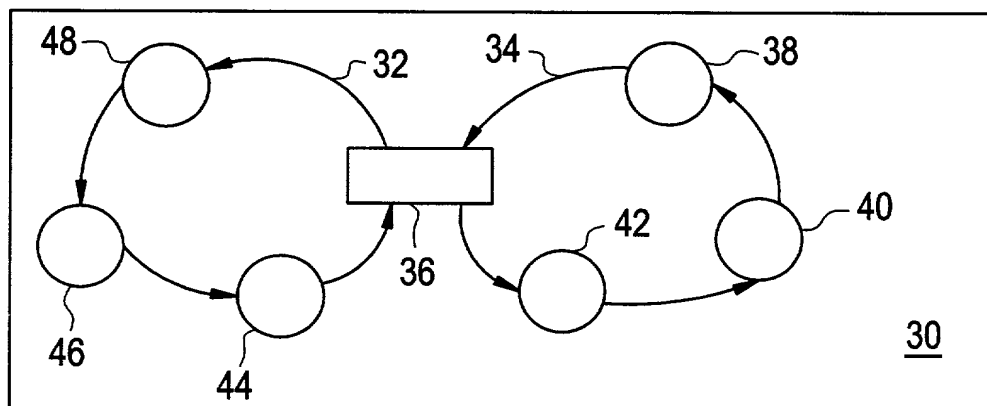


FIG. 3

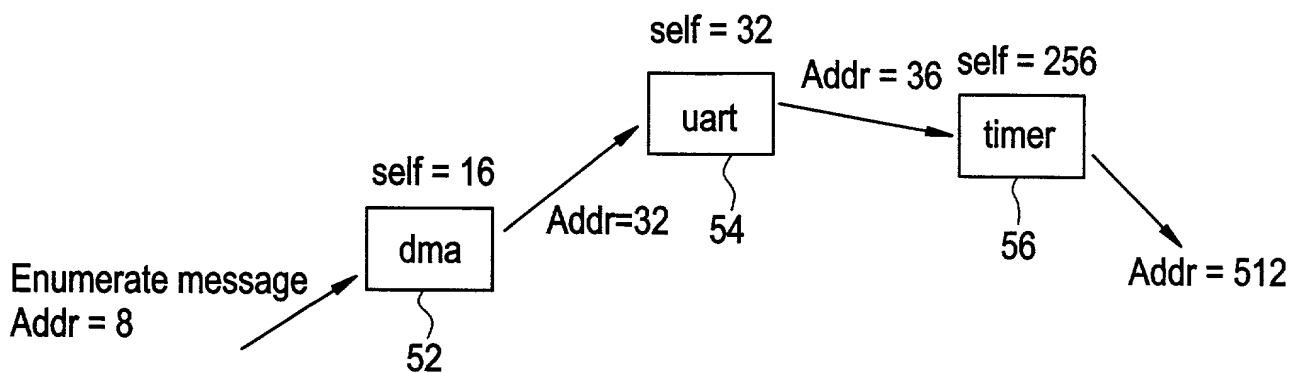


FIG. 4

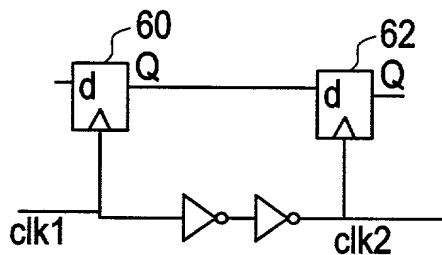
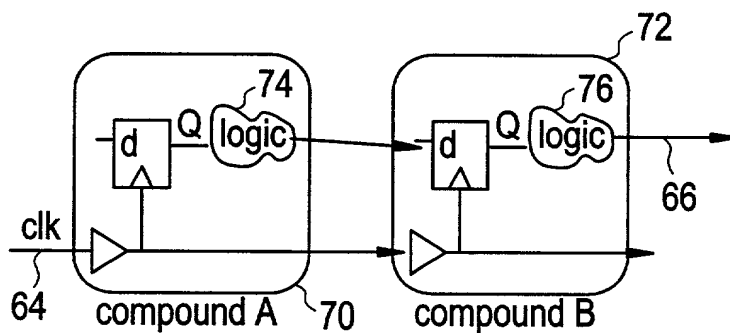


FIG. 5



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FIG. 6

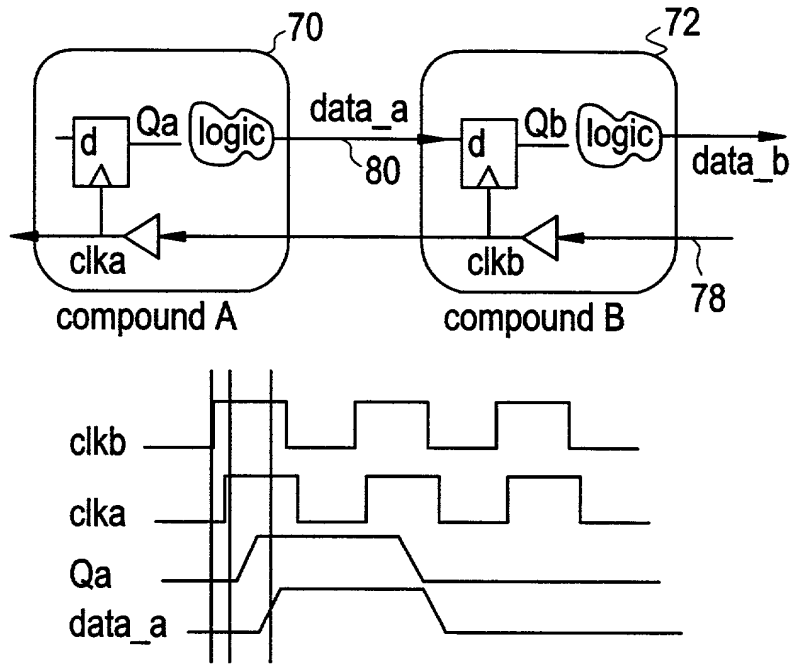
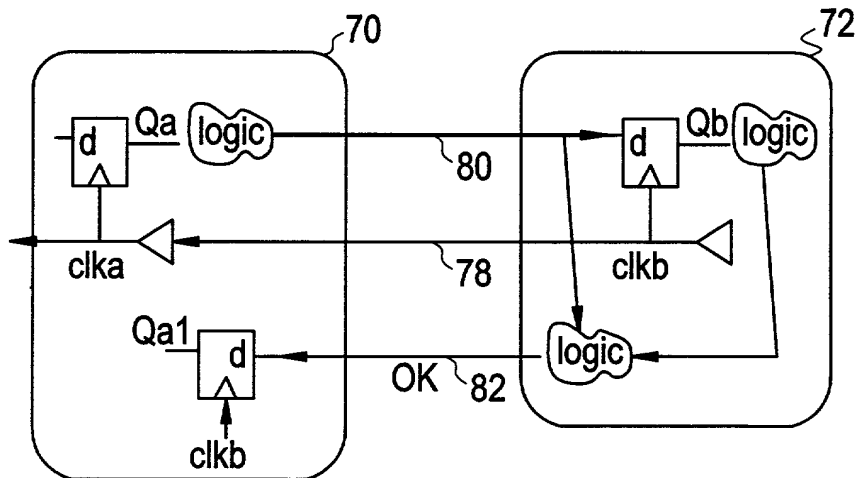


FIG. 7



2009260" 62E4900T

FIG. 8

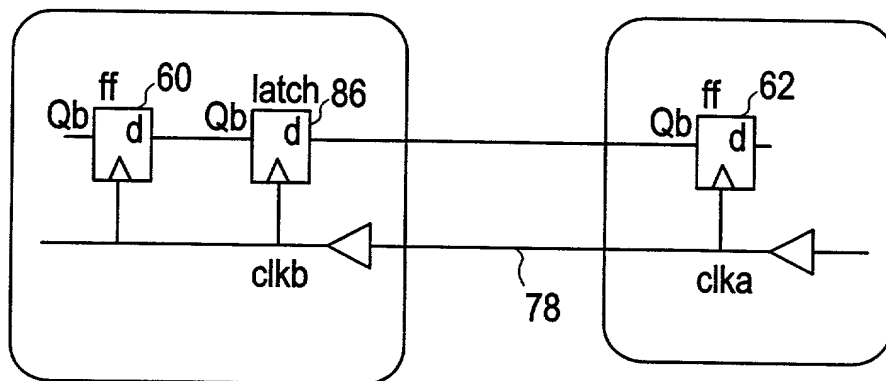


FIG. 9

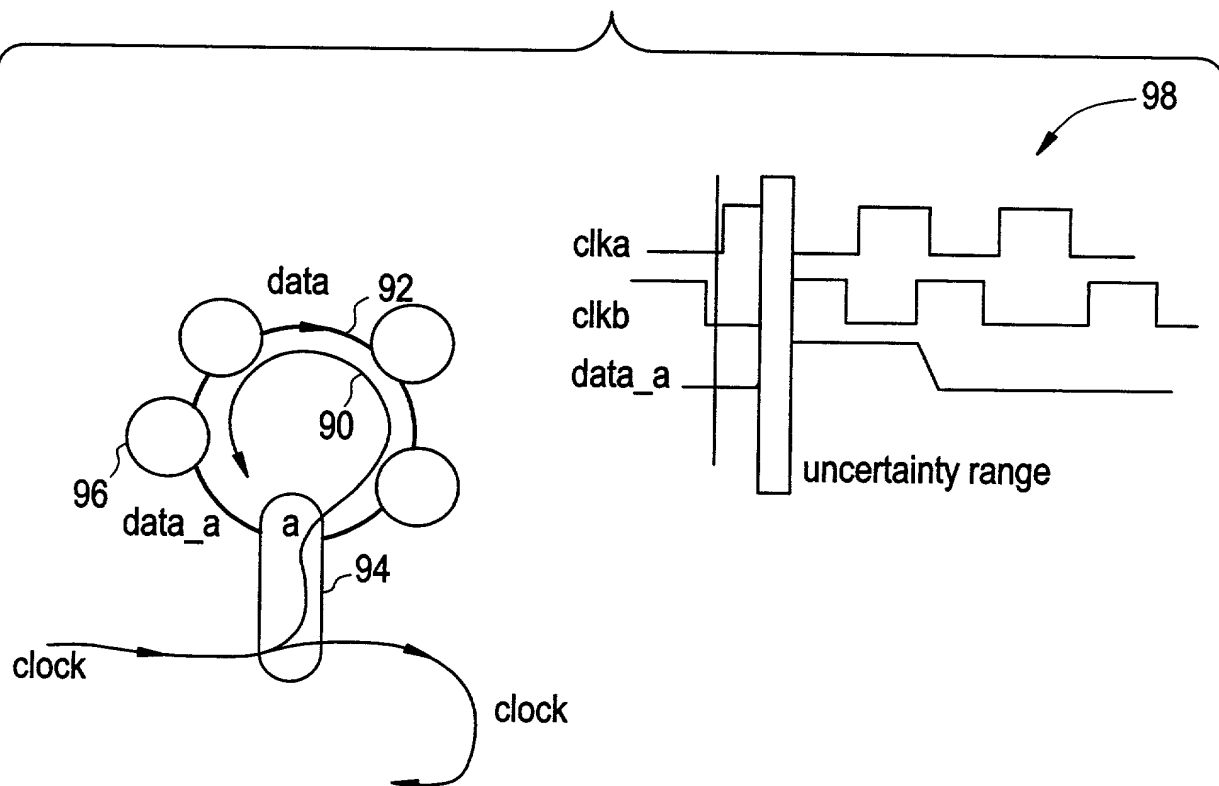


FIG. 10

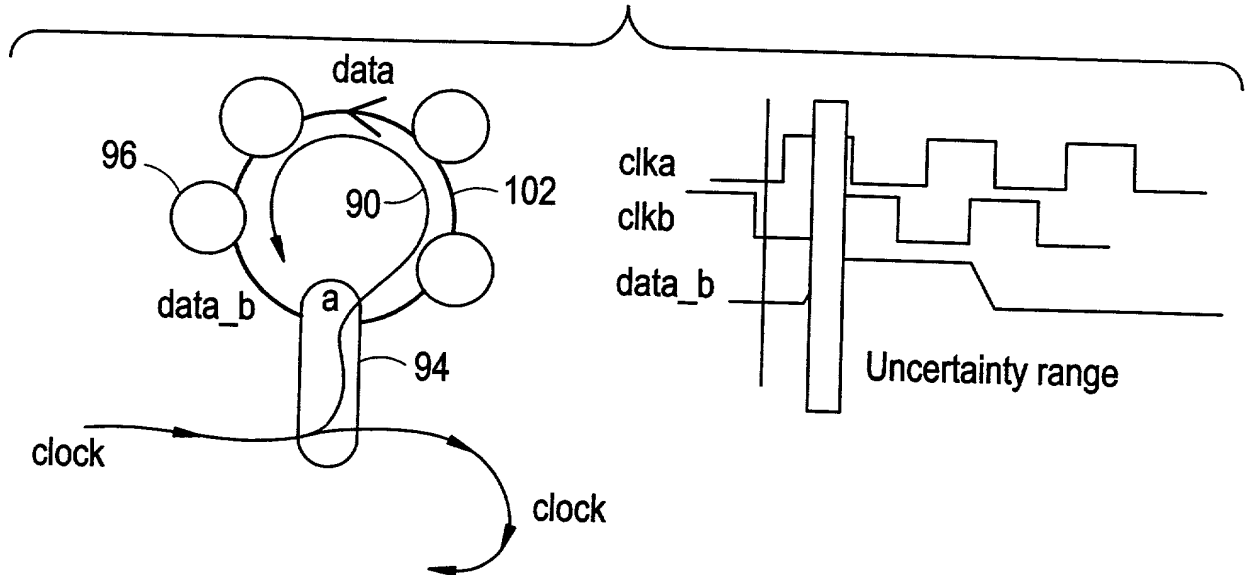


FIG. 11

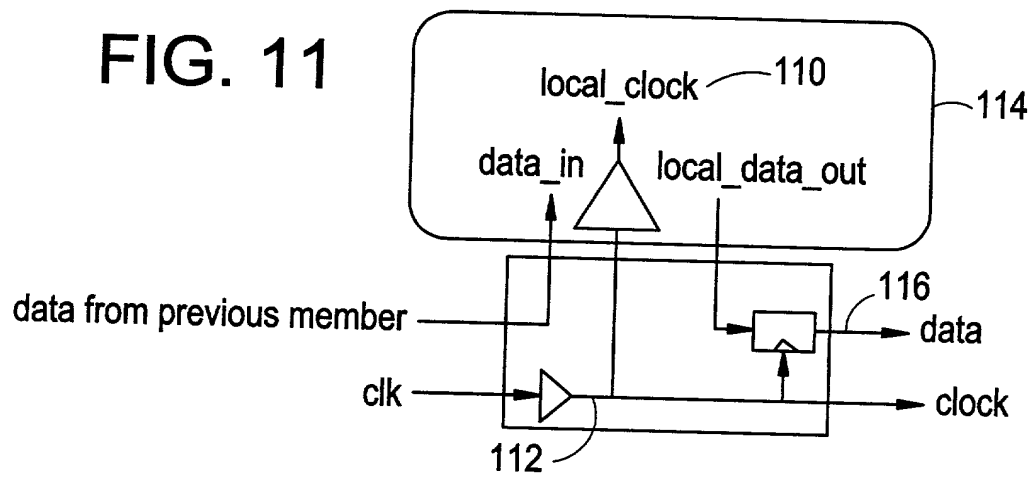


FIG. 12

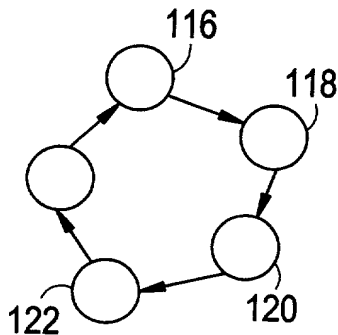


FIG. 13

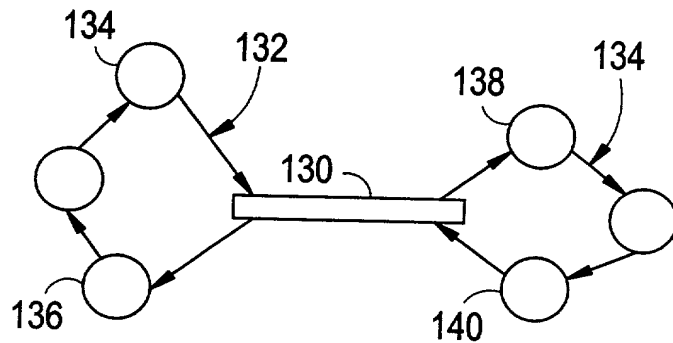


FIG. 14

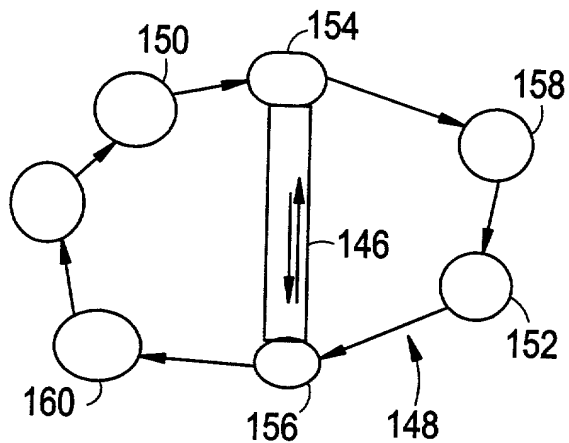


FIG. 15

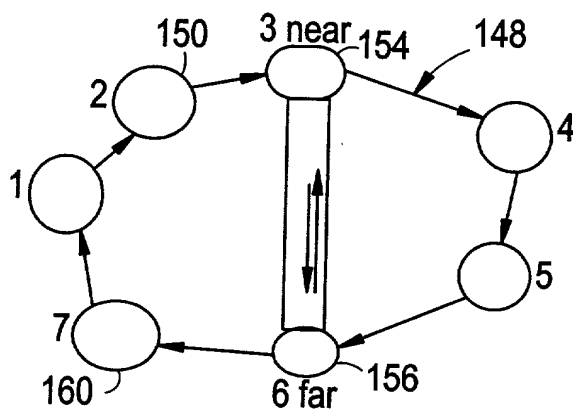
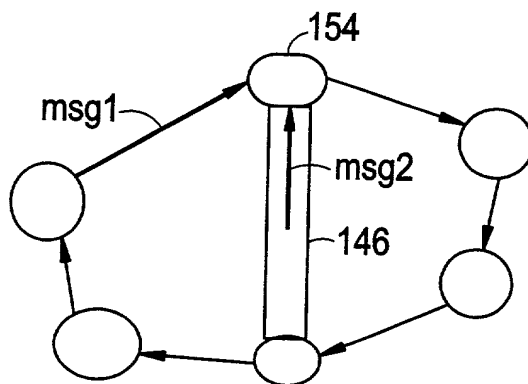


FIG. 16



209260" 62E49001

FIG. 17

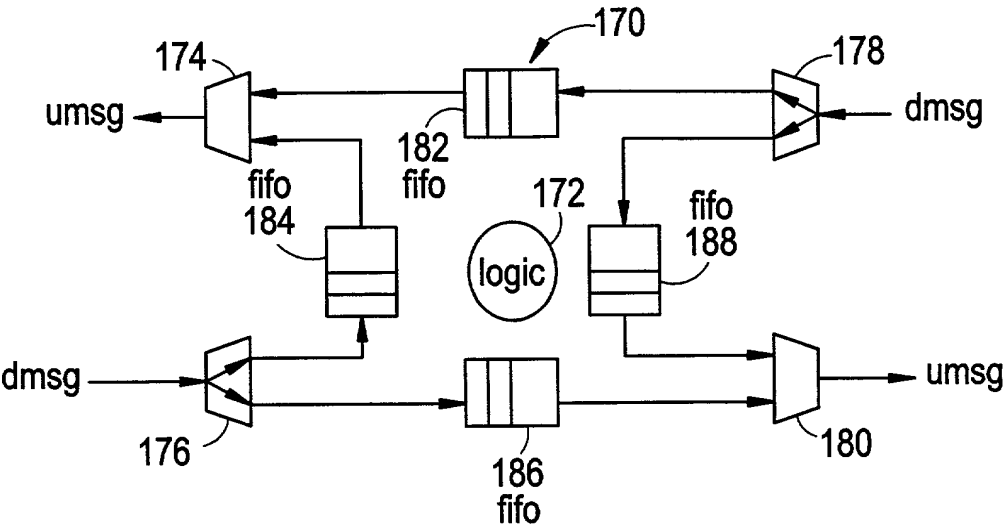


FIG. 18

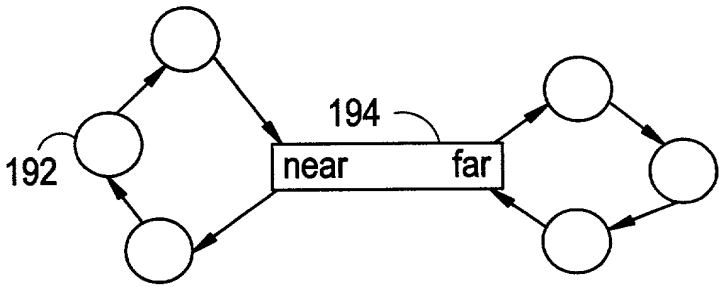


FIG. 19

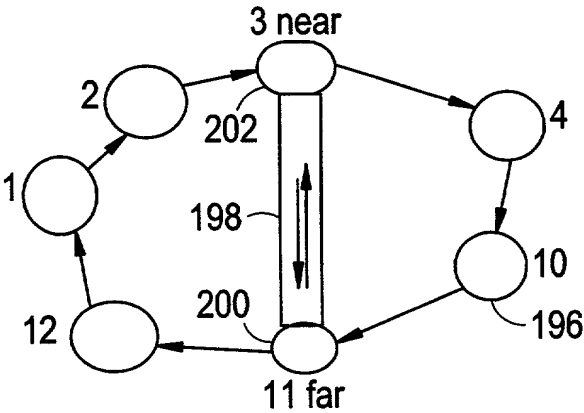


FIG. 20

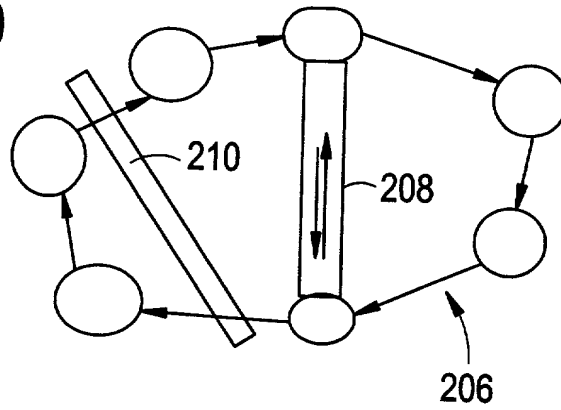


FIG. 21

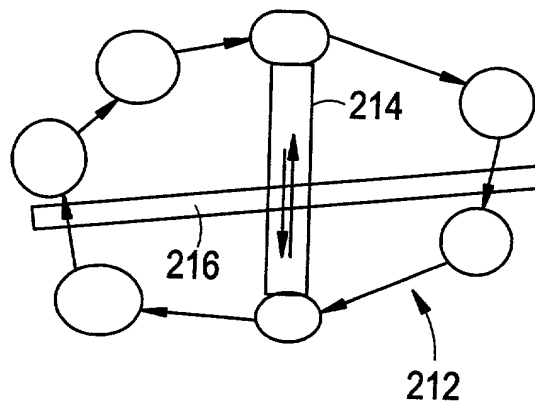


FIG. 22

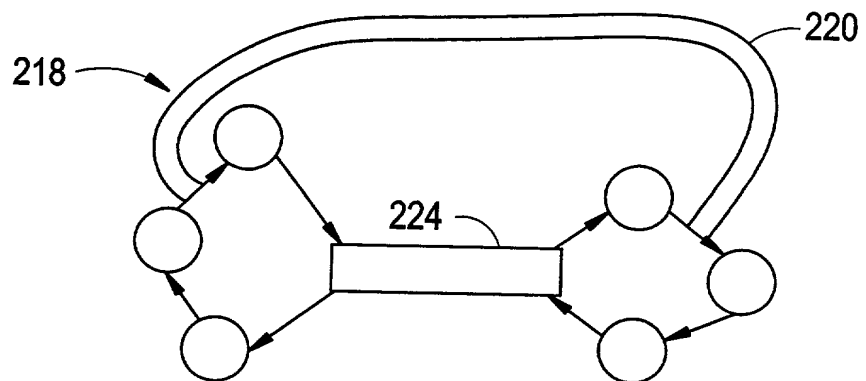




FIG. 23

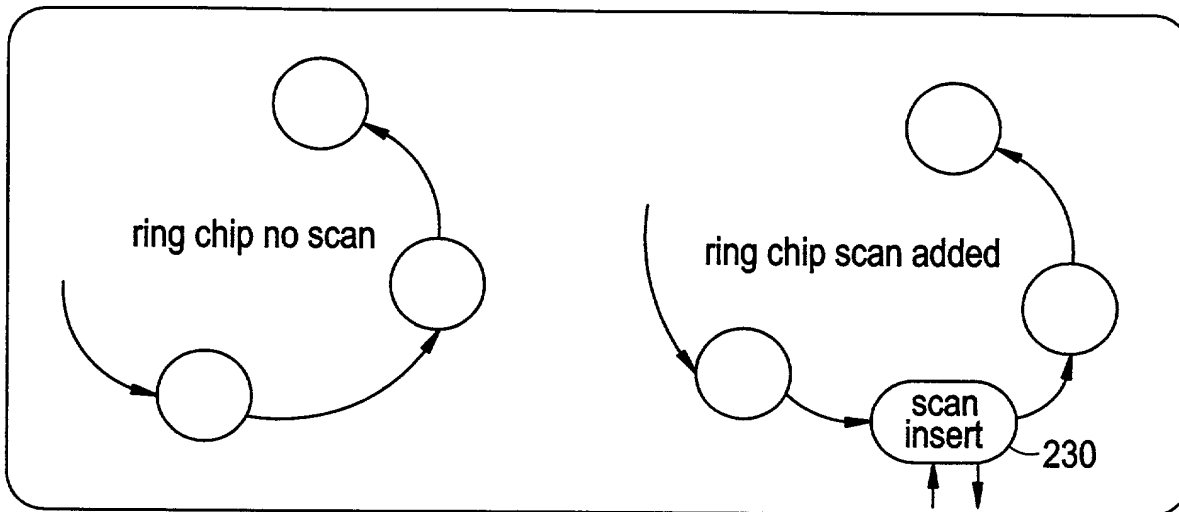


FIG. 24

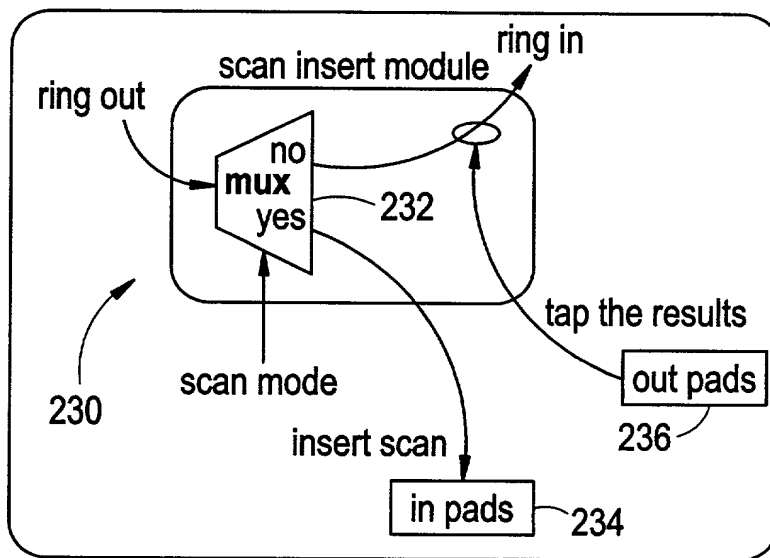
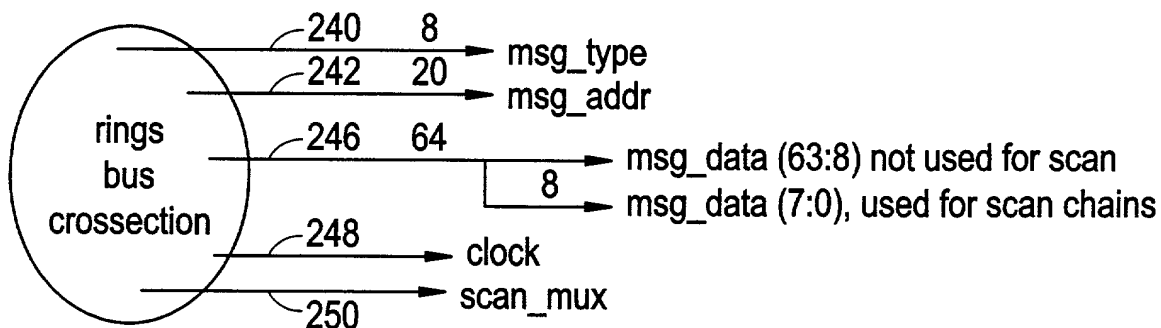


FIG. 25



209260" 624900T

FIG. 26

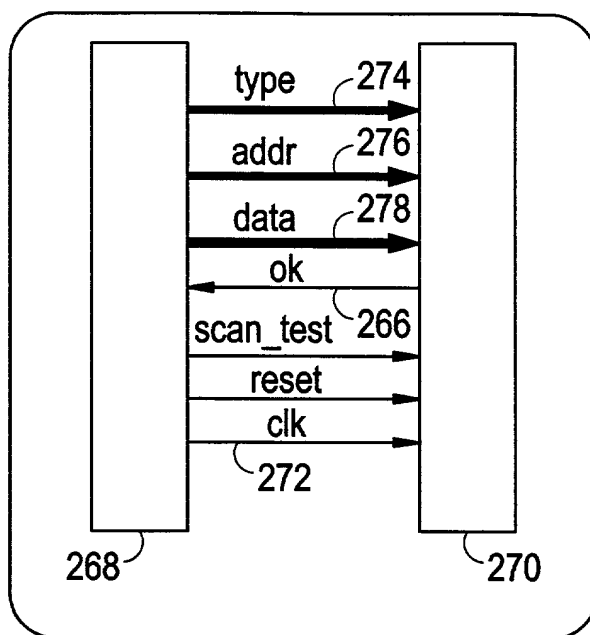


FIG. 27

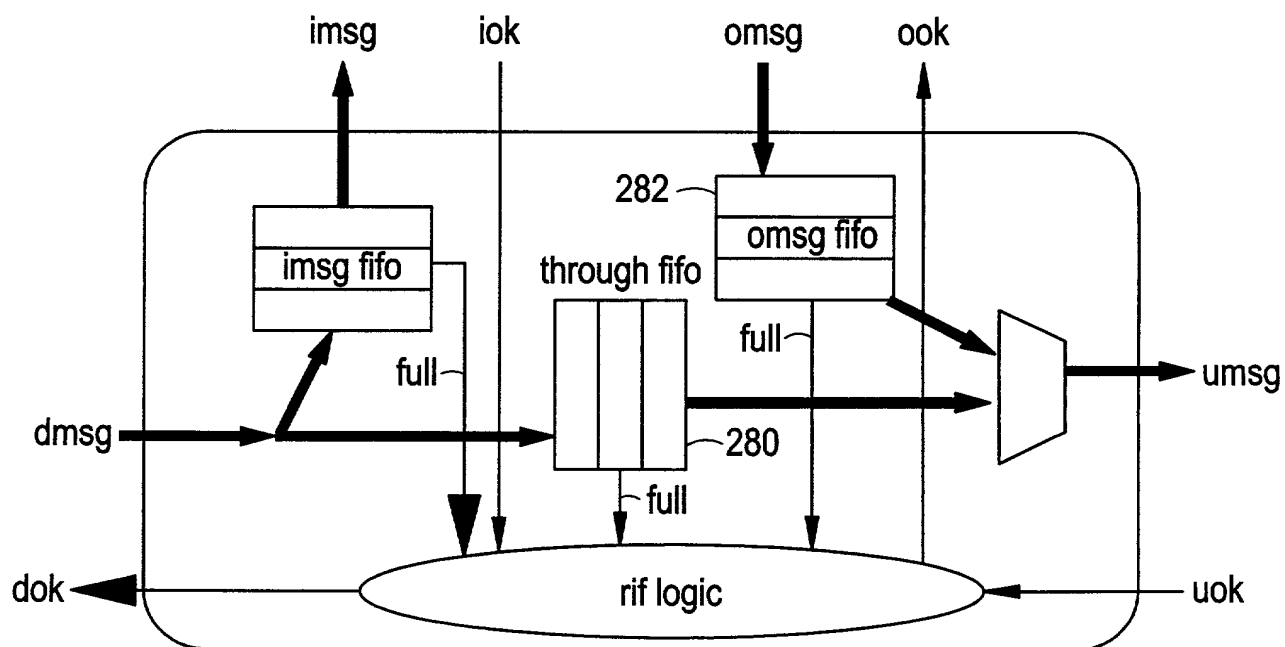


FIG. 28

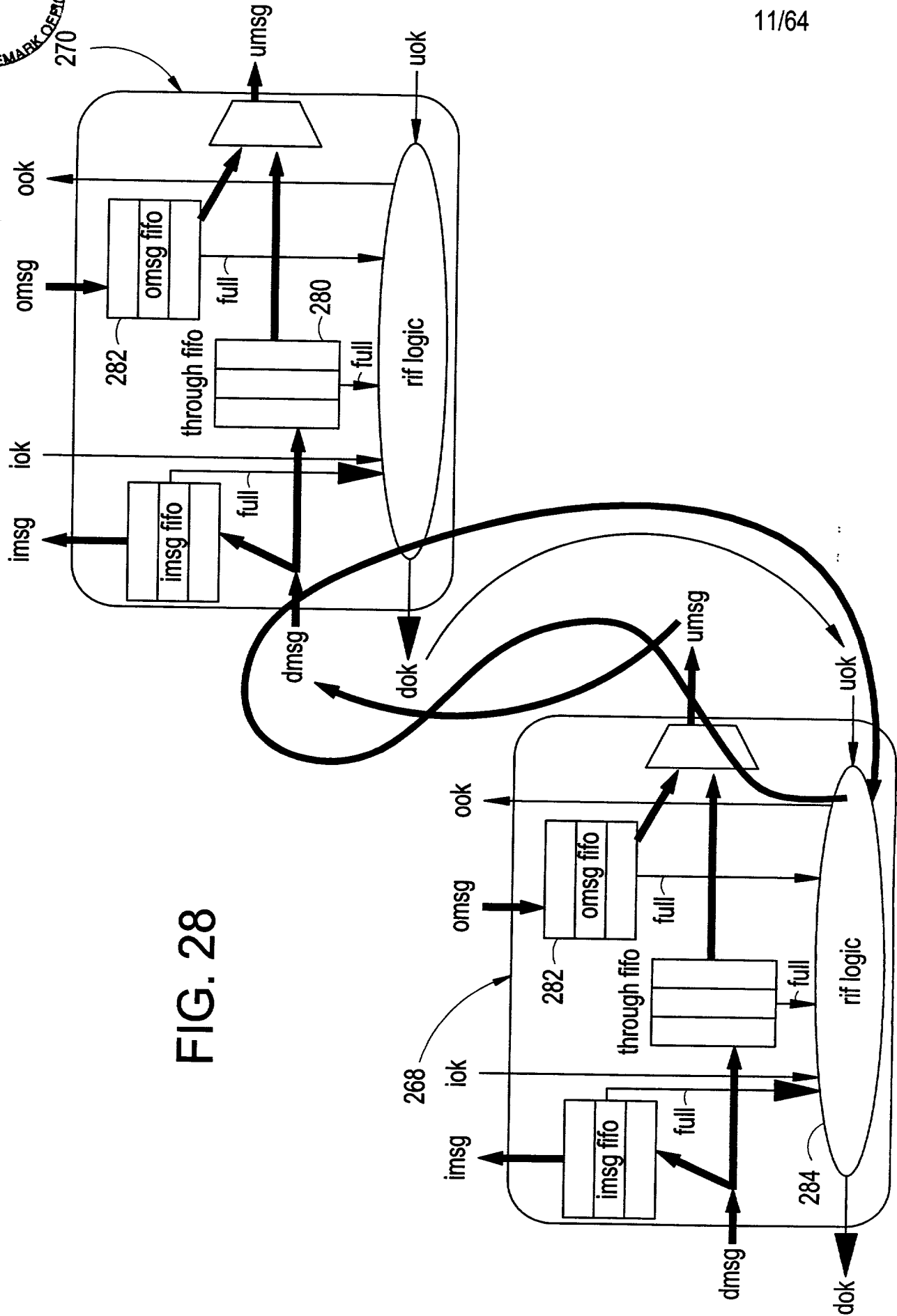
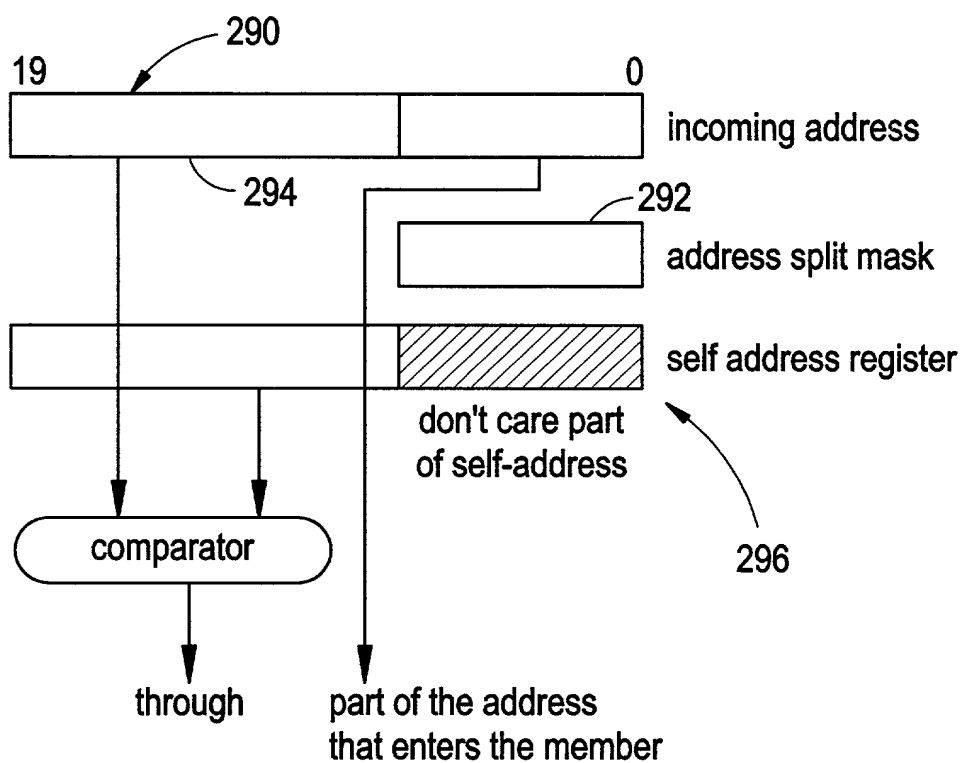


FIG. 29



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FIG. 30

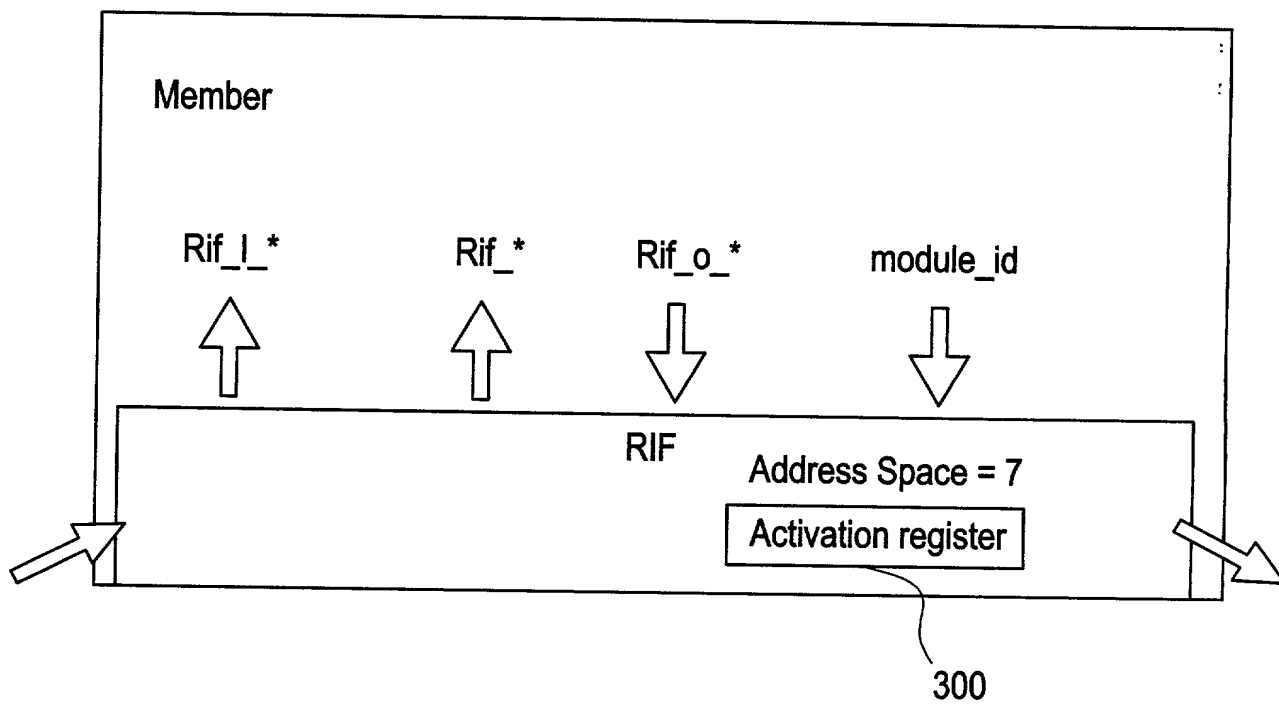


FIG. 31

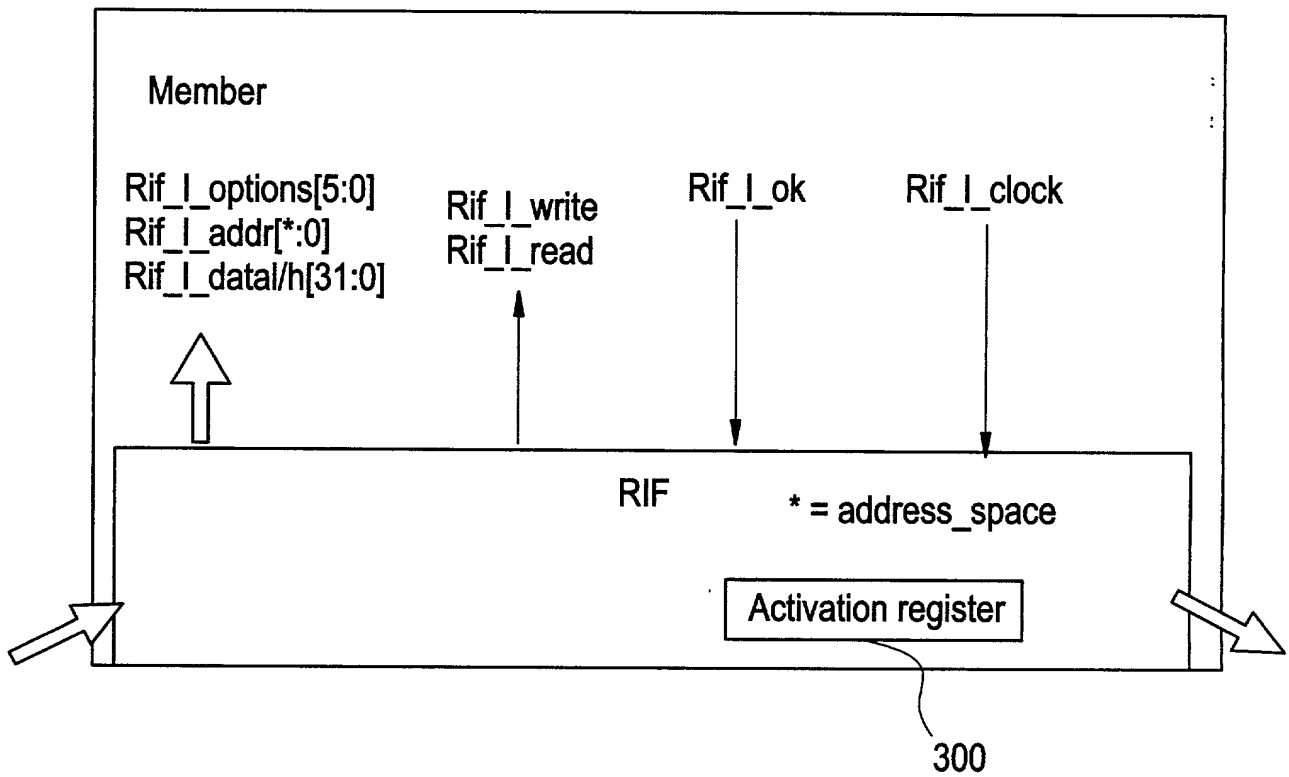


FIG. 32

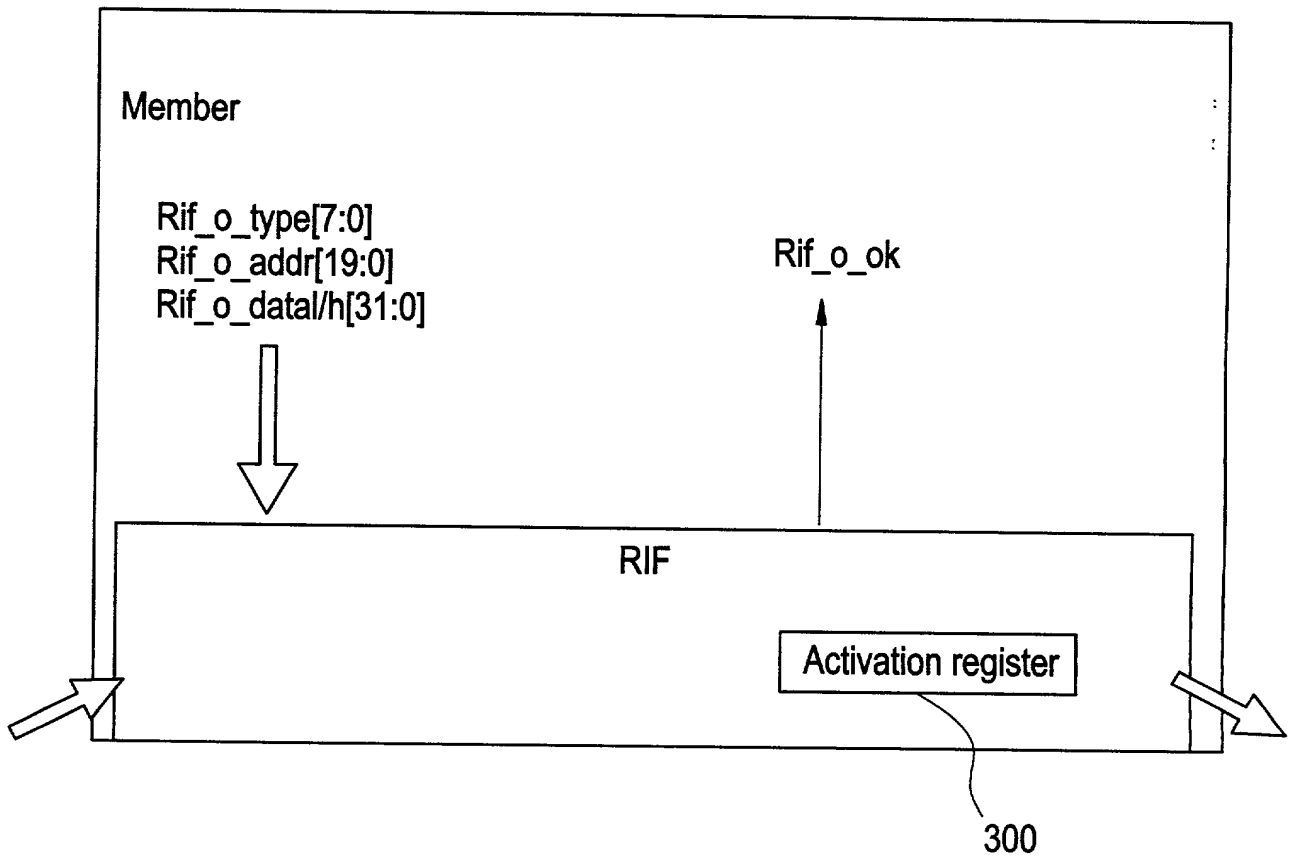


FIG. 33

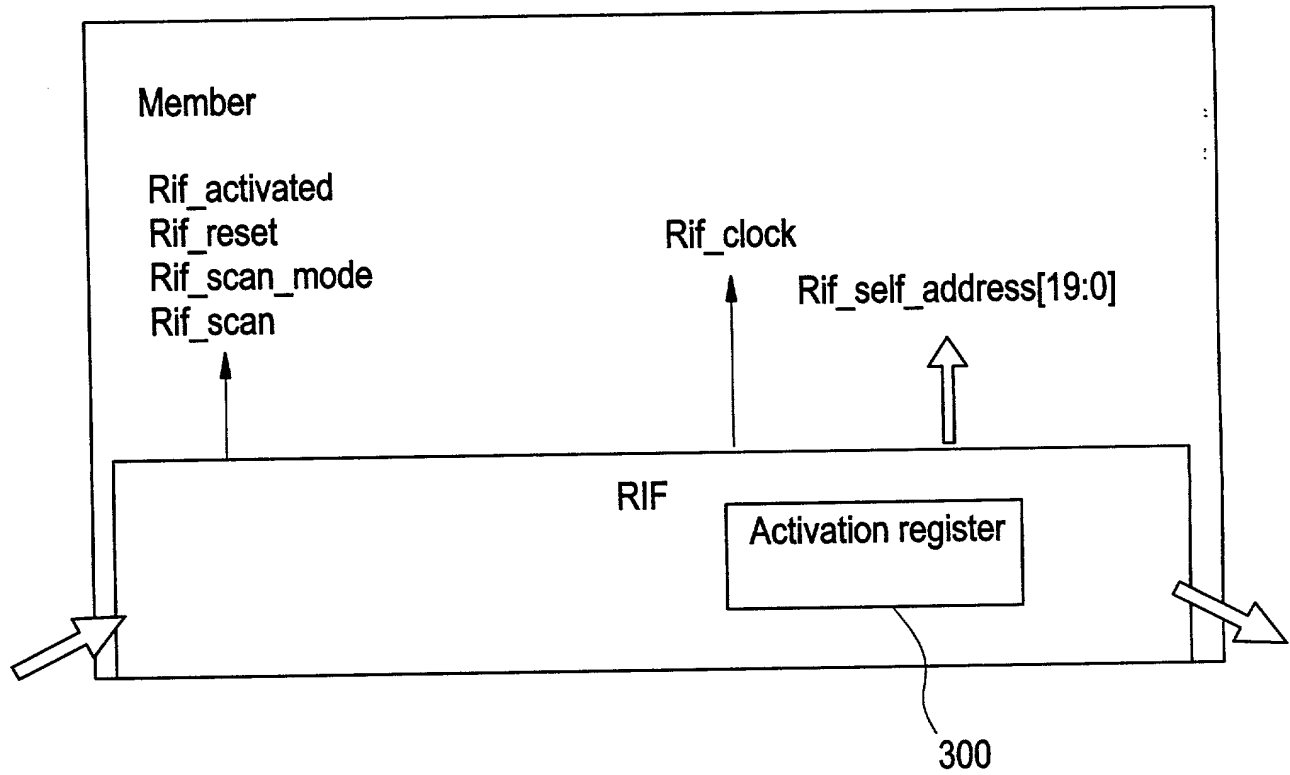




FIG. 34

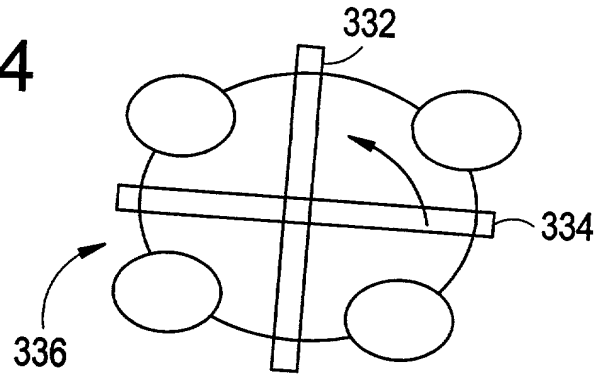


FIG. 35

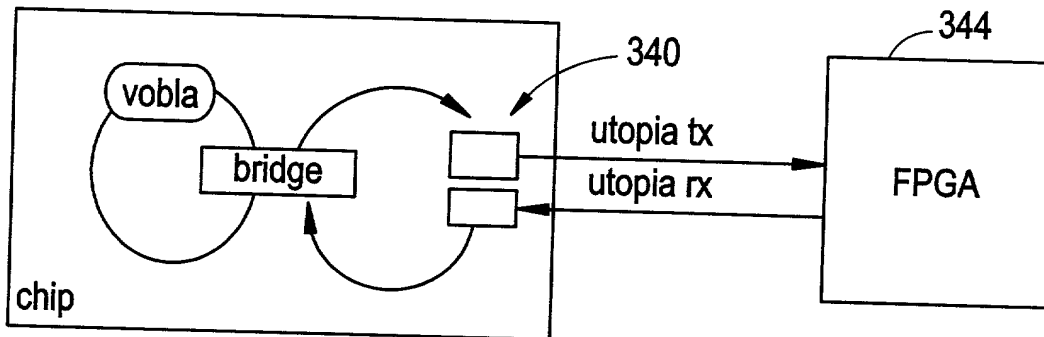


FIG. 36

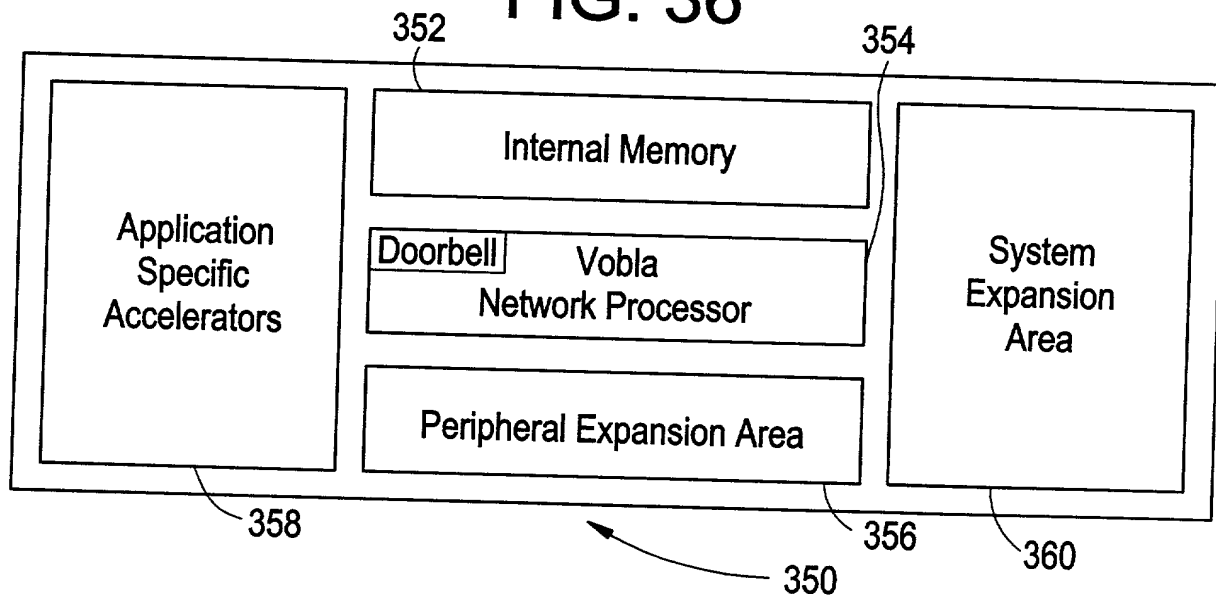
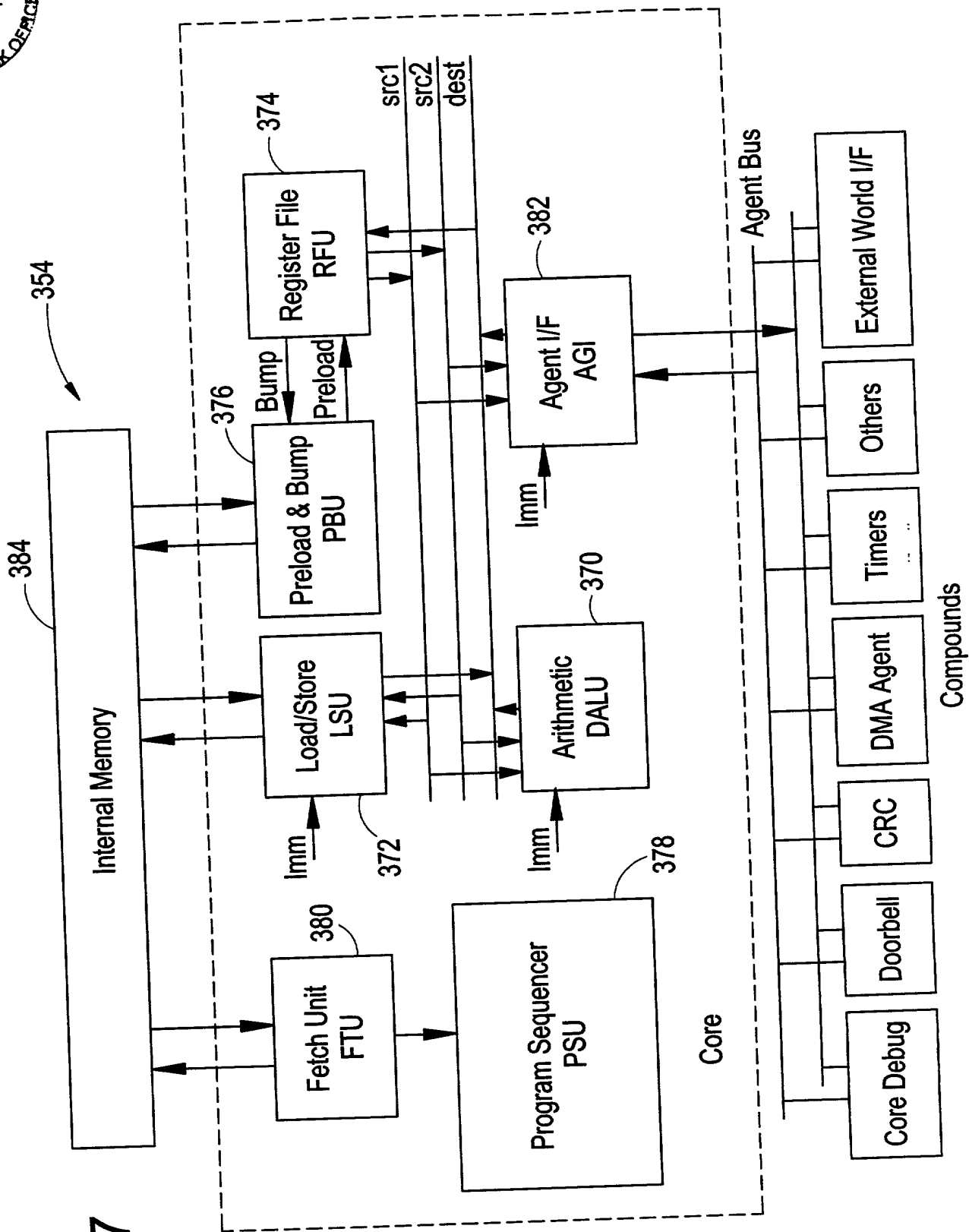


FIG. 37



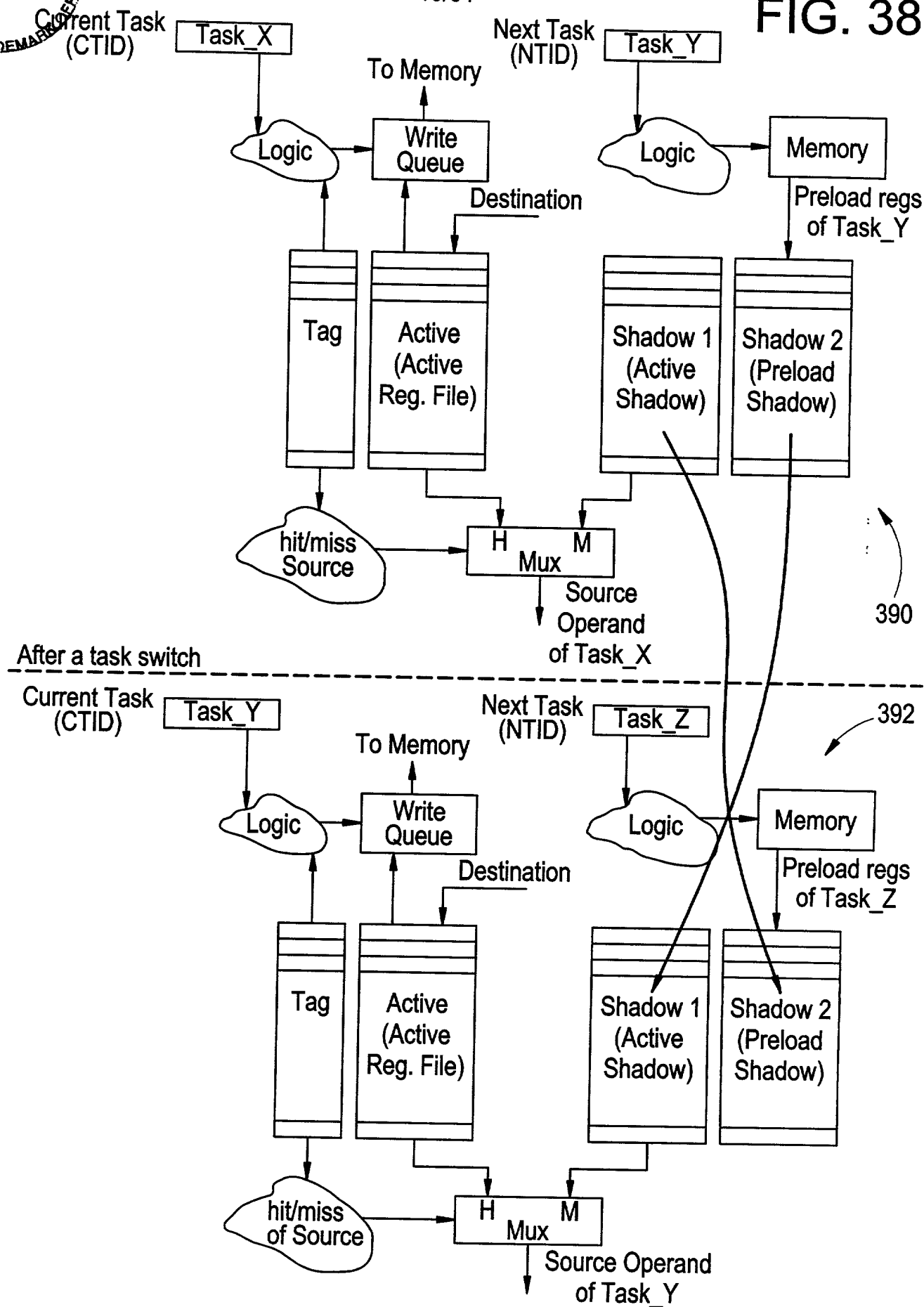


FIG. 39

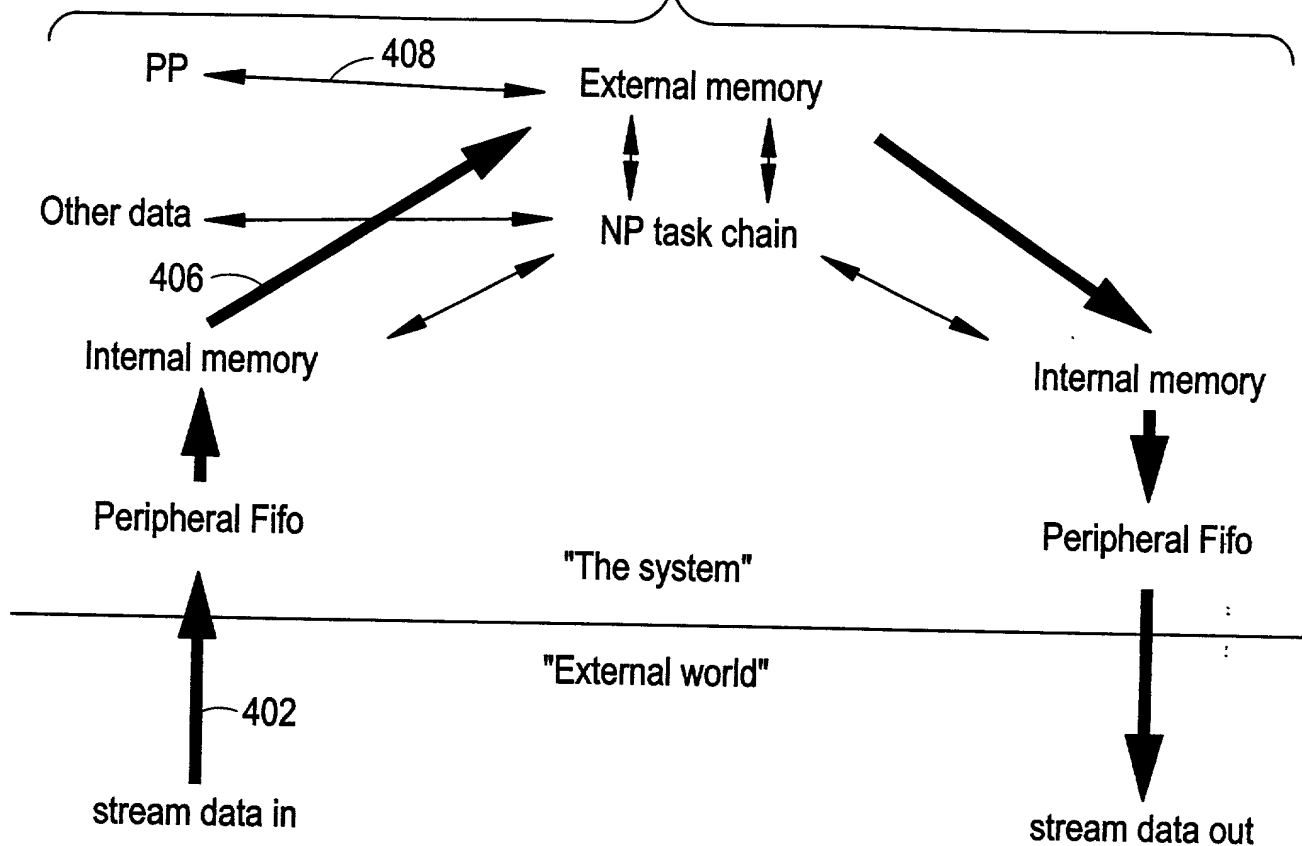


FIG. 40

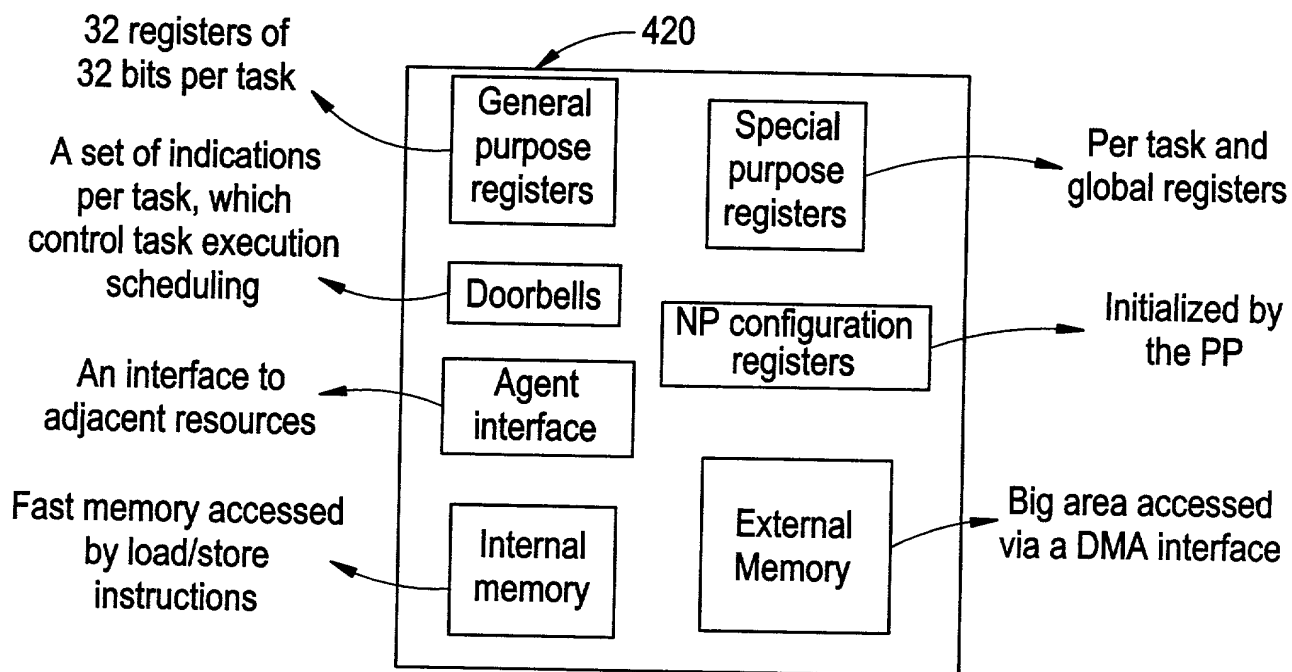
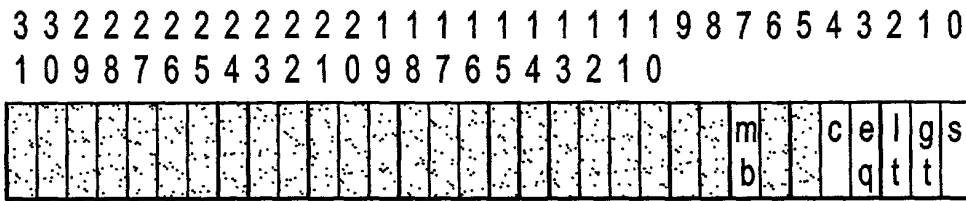


FIG. 41

R1 register



s - sticky bit  
 eq - equal/zero  
 lt - less than/negative  
 gt - greater than/positive  
 c - carry  
 mb - reflection of the RAM mult-reader busy indication

430

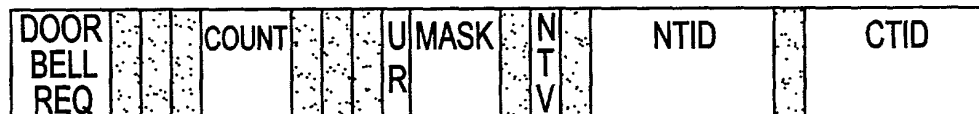
FIG. 42

REFETCH SPR  
(spr index - 0)



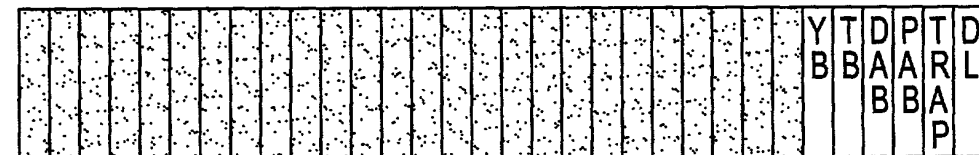
440

TASK SPR  
(spr index - 1)



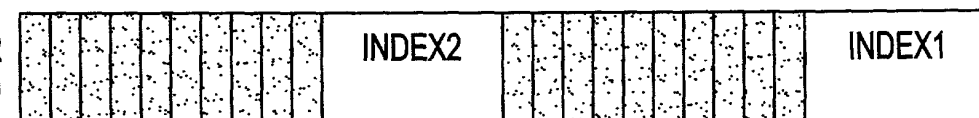
442

TRAP SPR  
(spr index - 2)



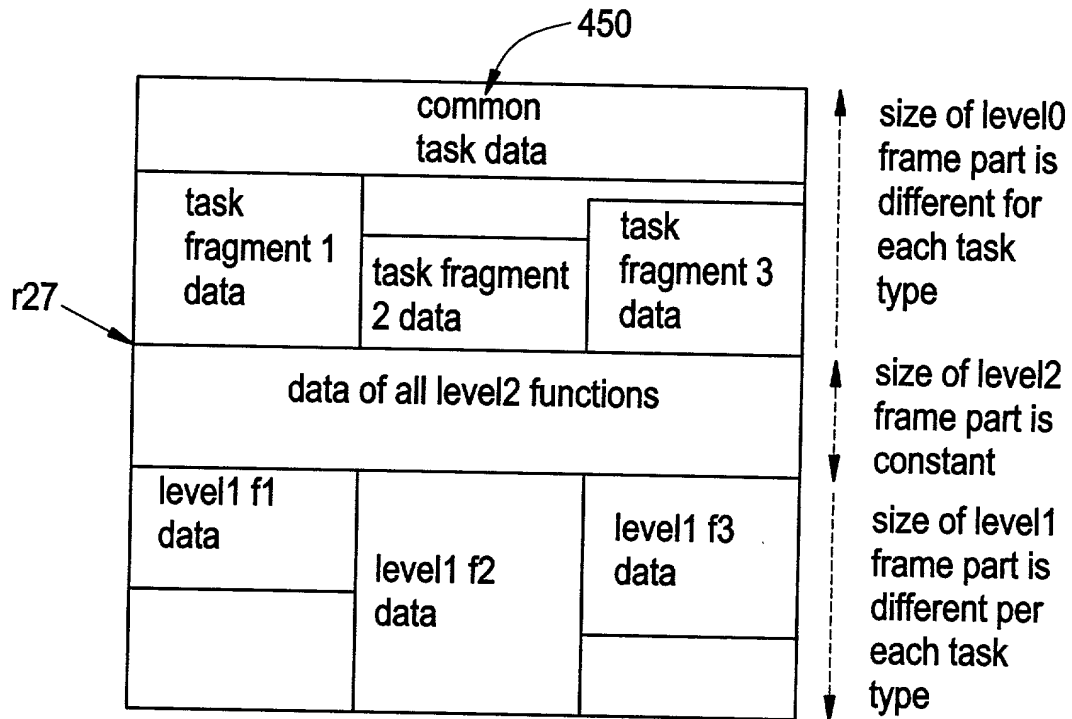
444

MINDEX SPR  
(spr index - 3)



446

FIG. 43



209260" 6249001

FIG. 44

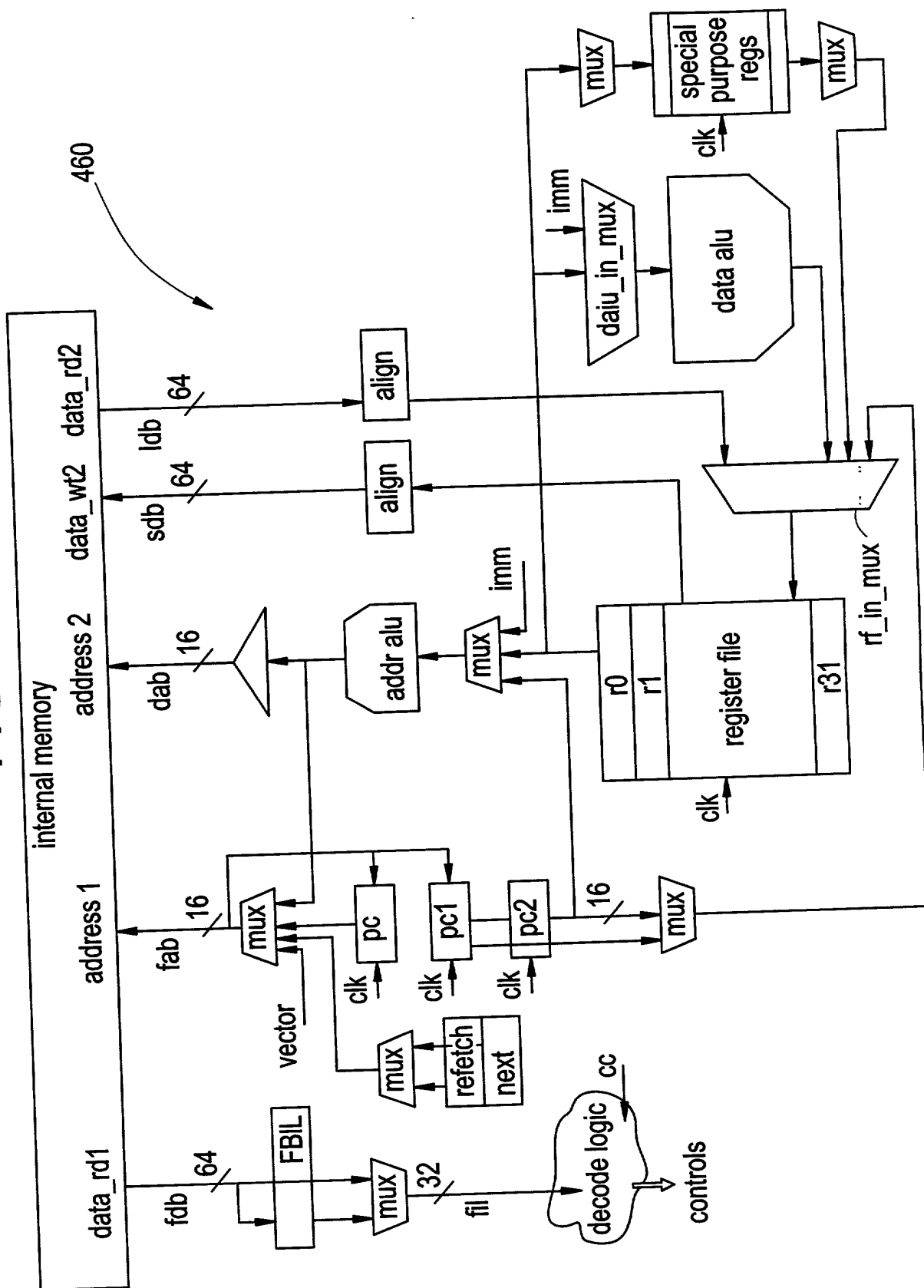
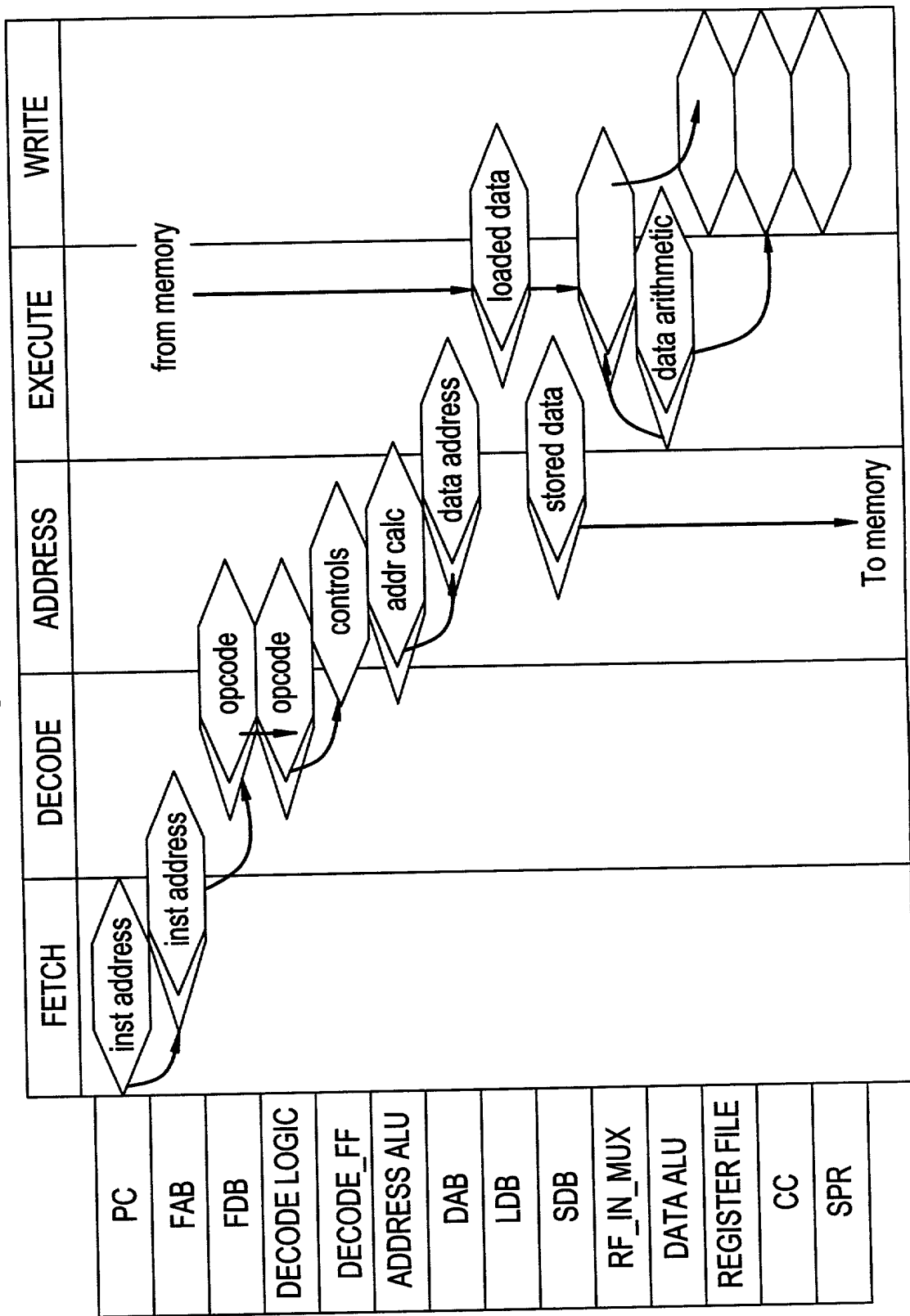


FIG. 45



480



FIG. 46

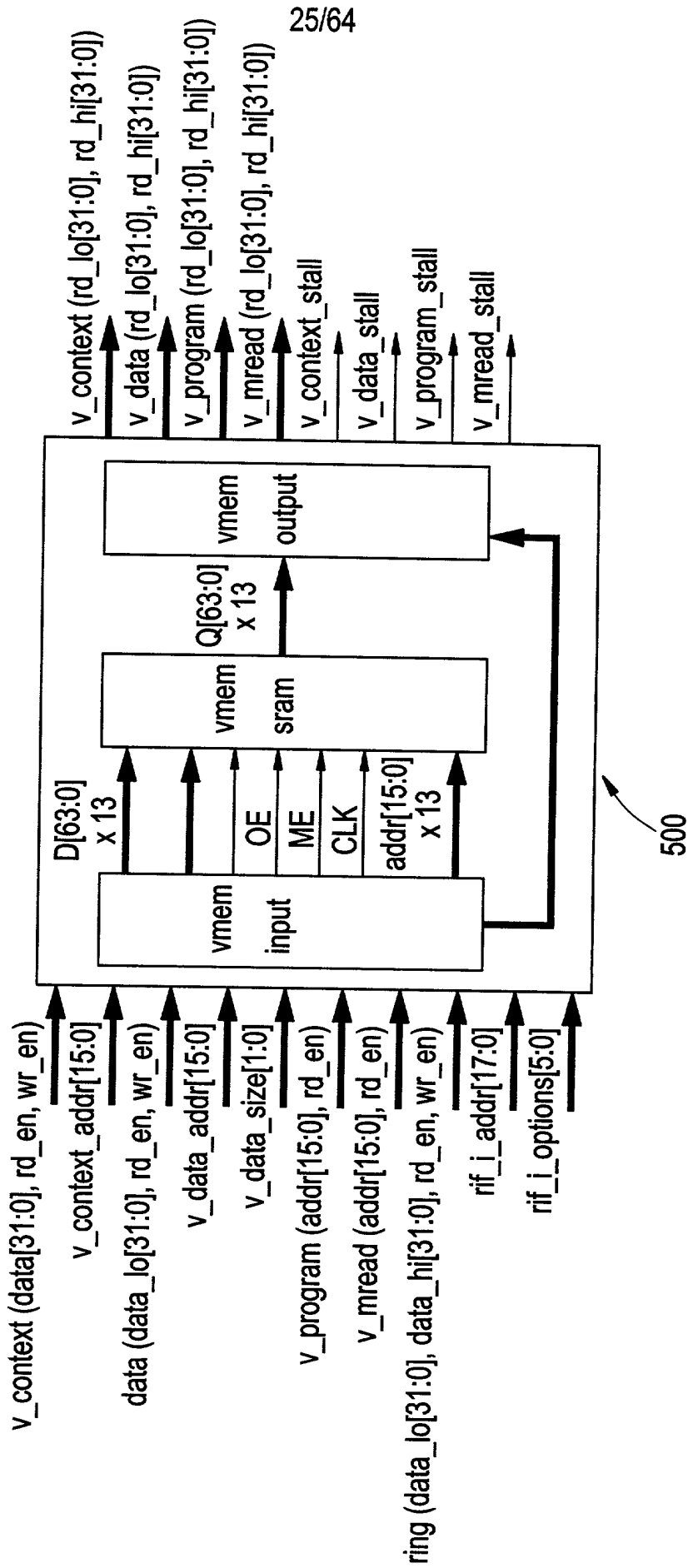


FIG. 47

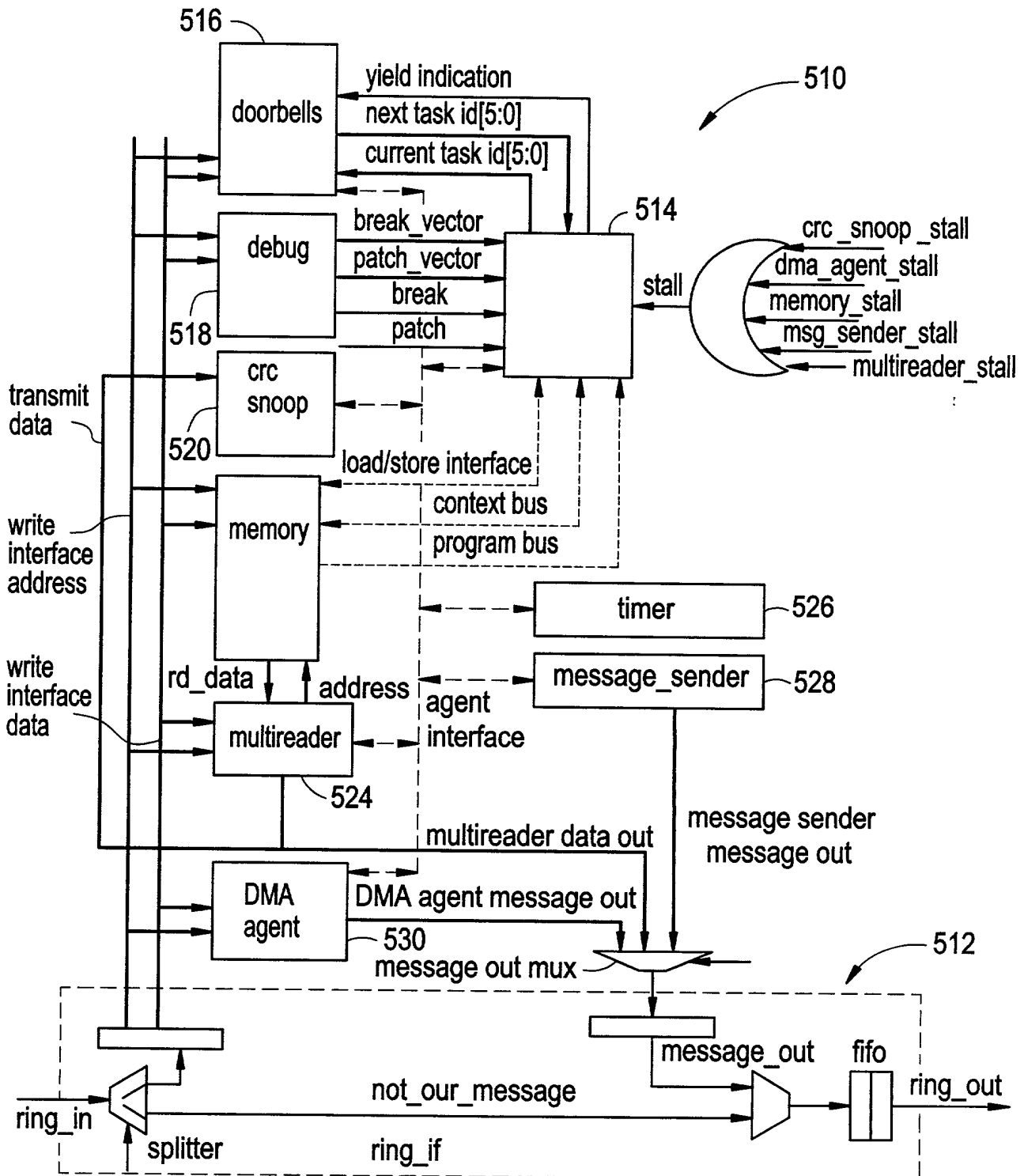


FIG. 48

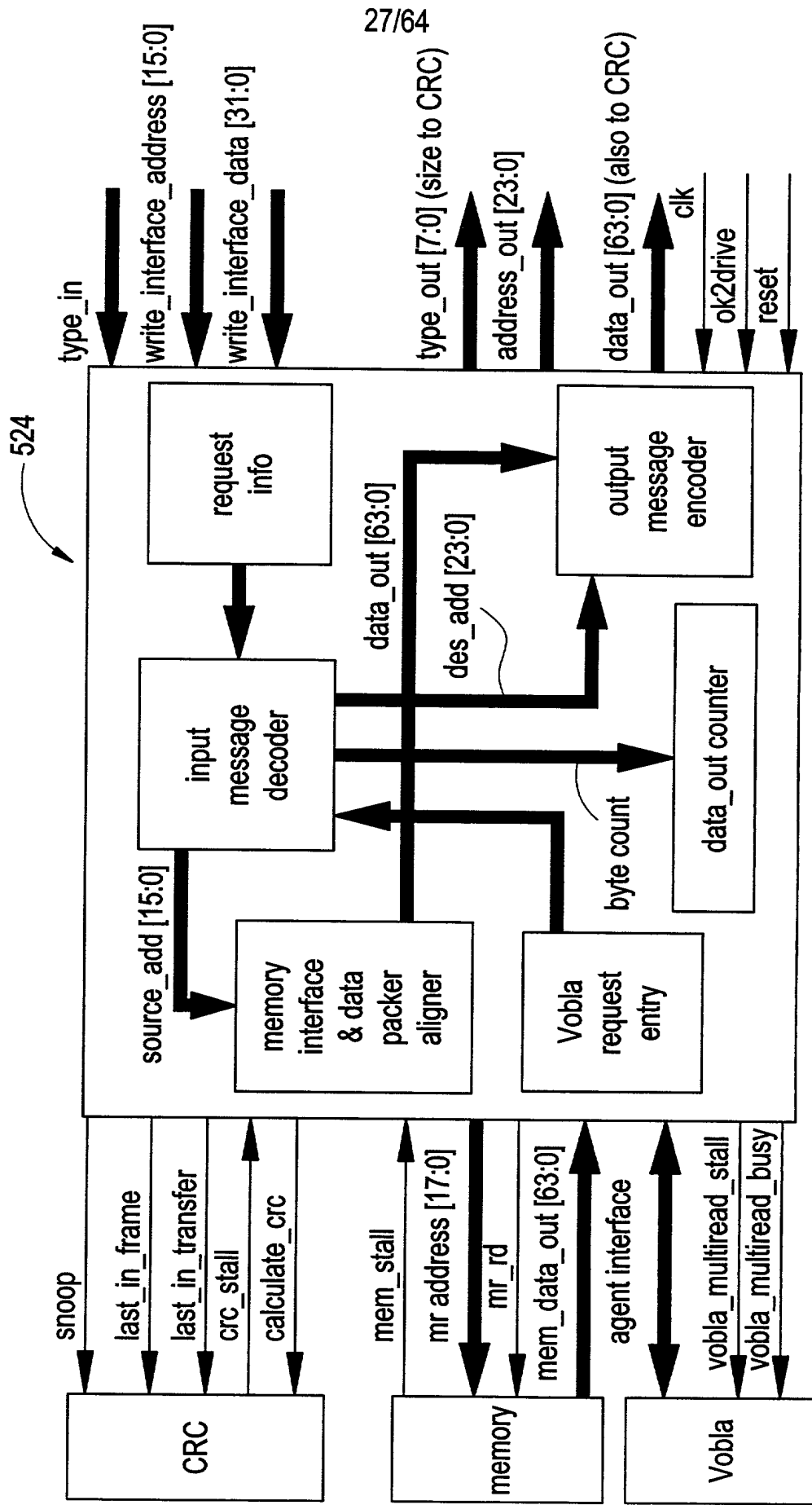


FIG. 49

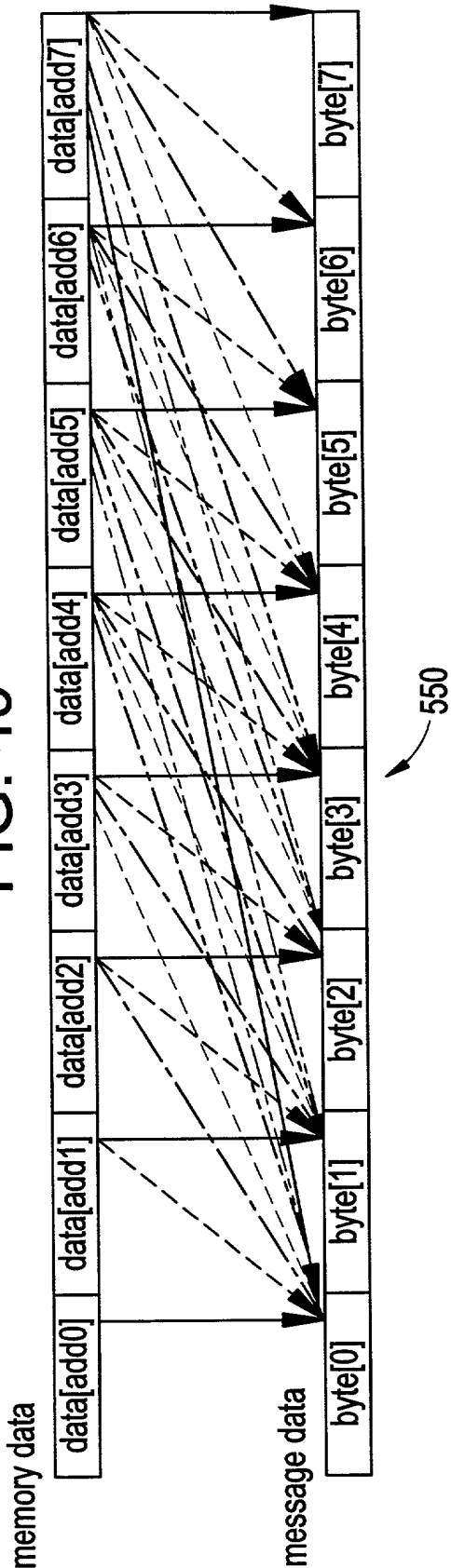
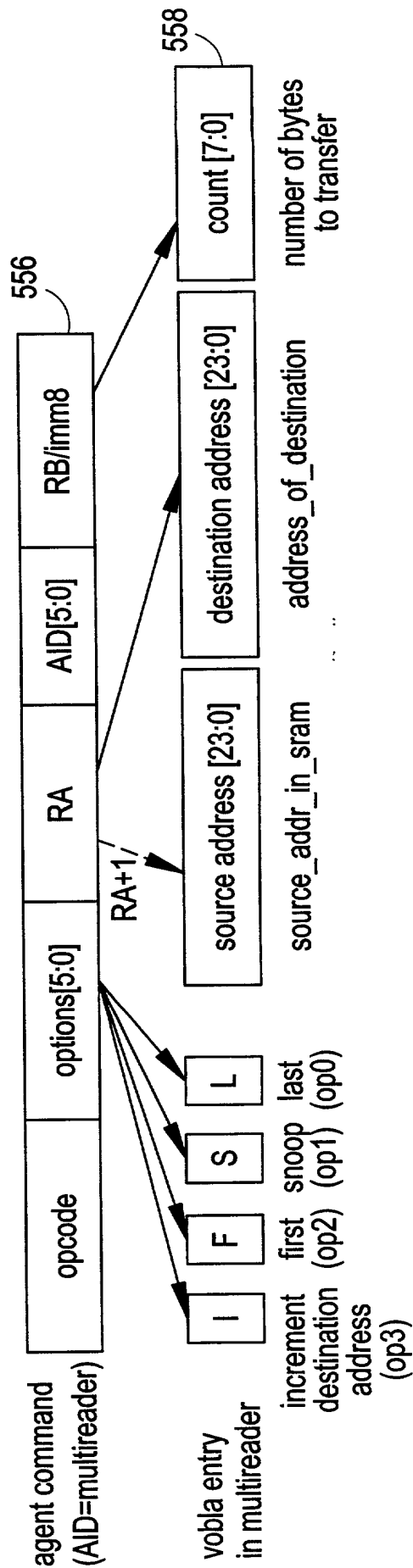


FIG. 50



2009260 "SECRET"

FIG. 51

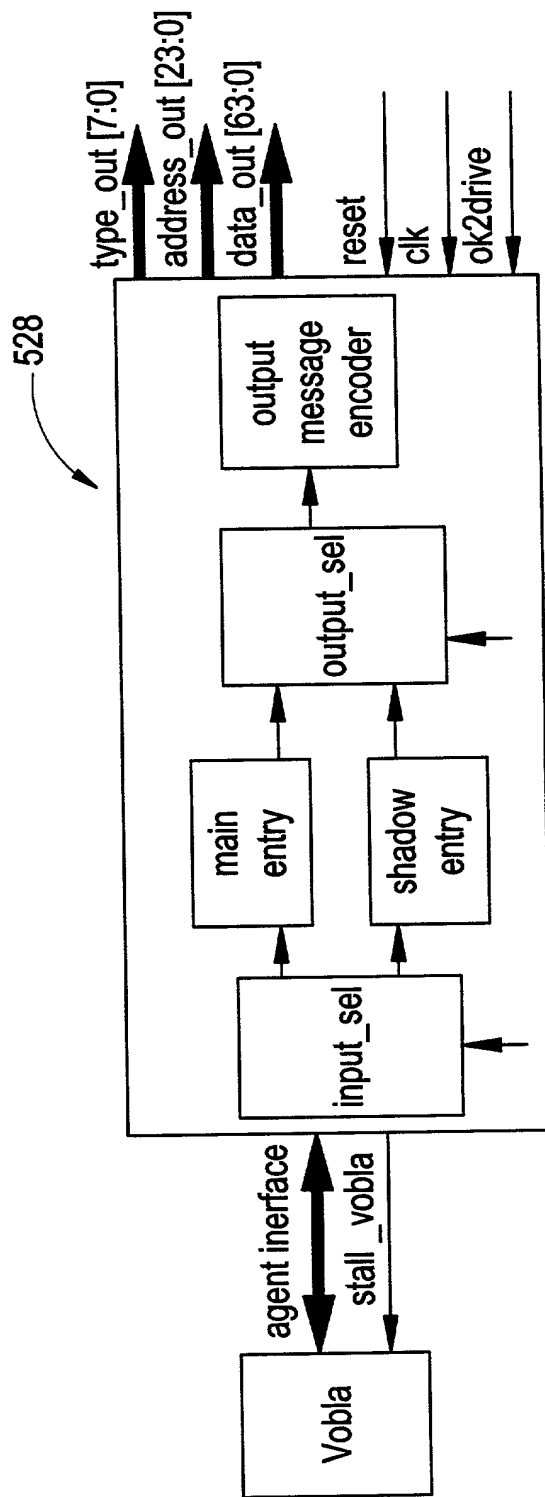
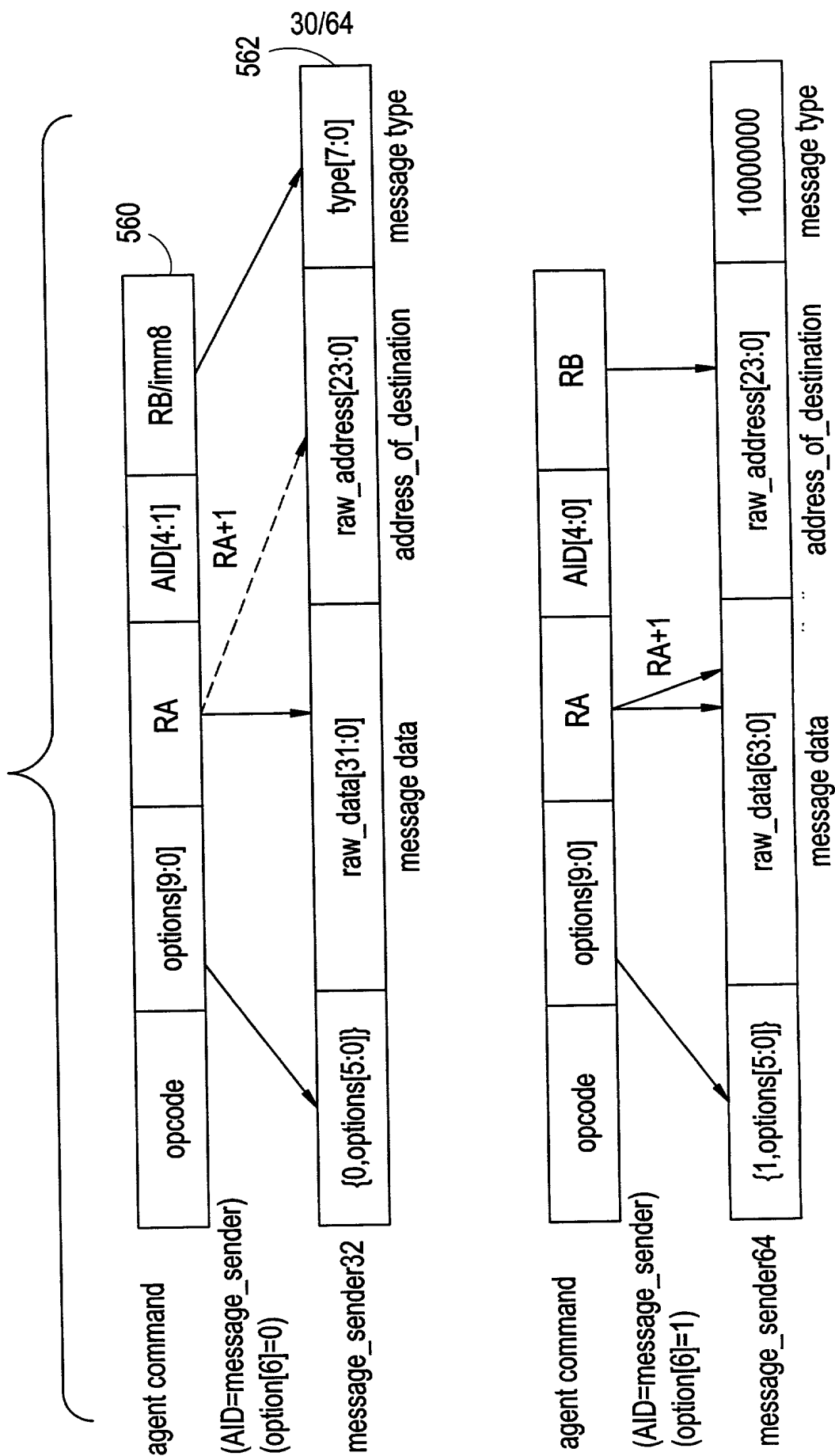


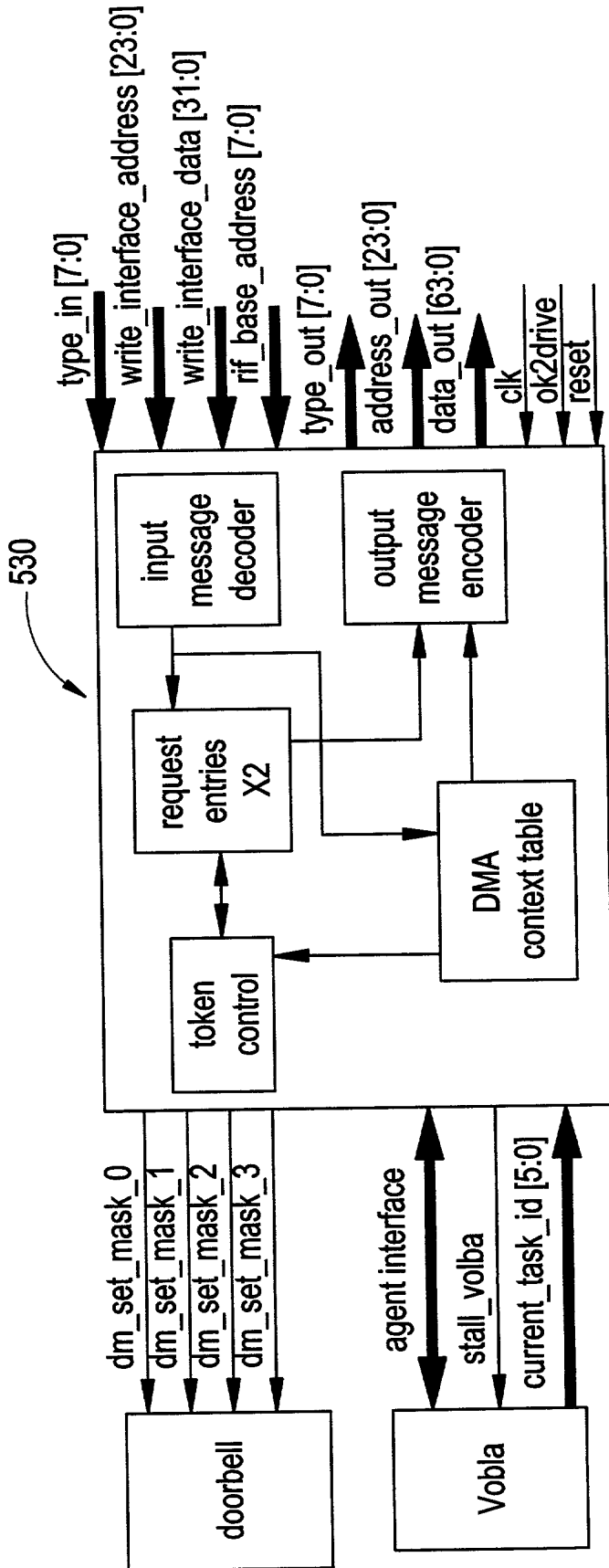


FIG. 52



2009260 "SET-4900T"

FIG. 53



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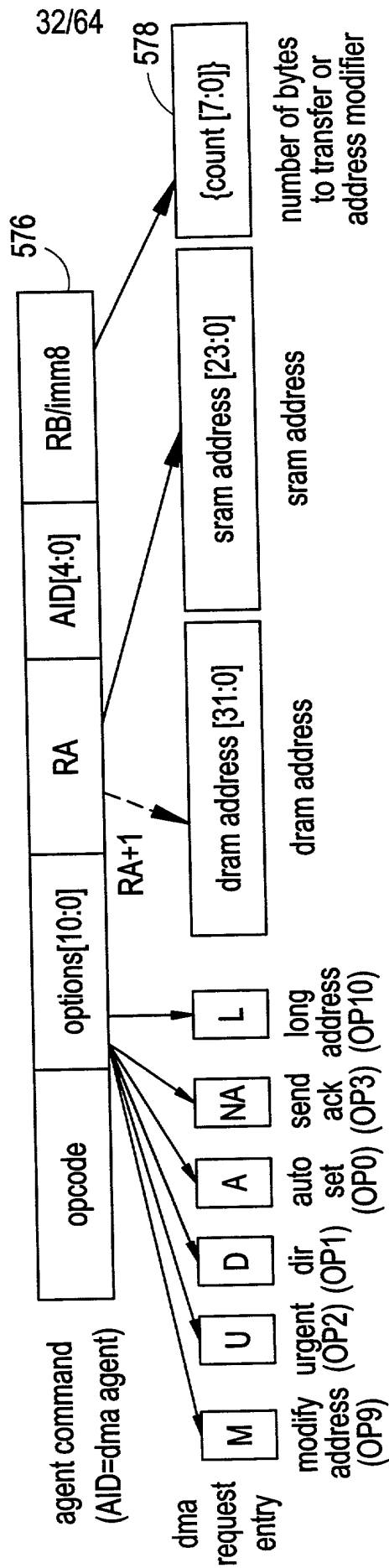




FIG. 55

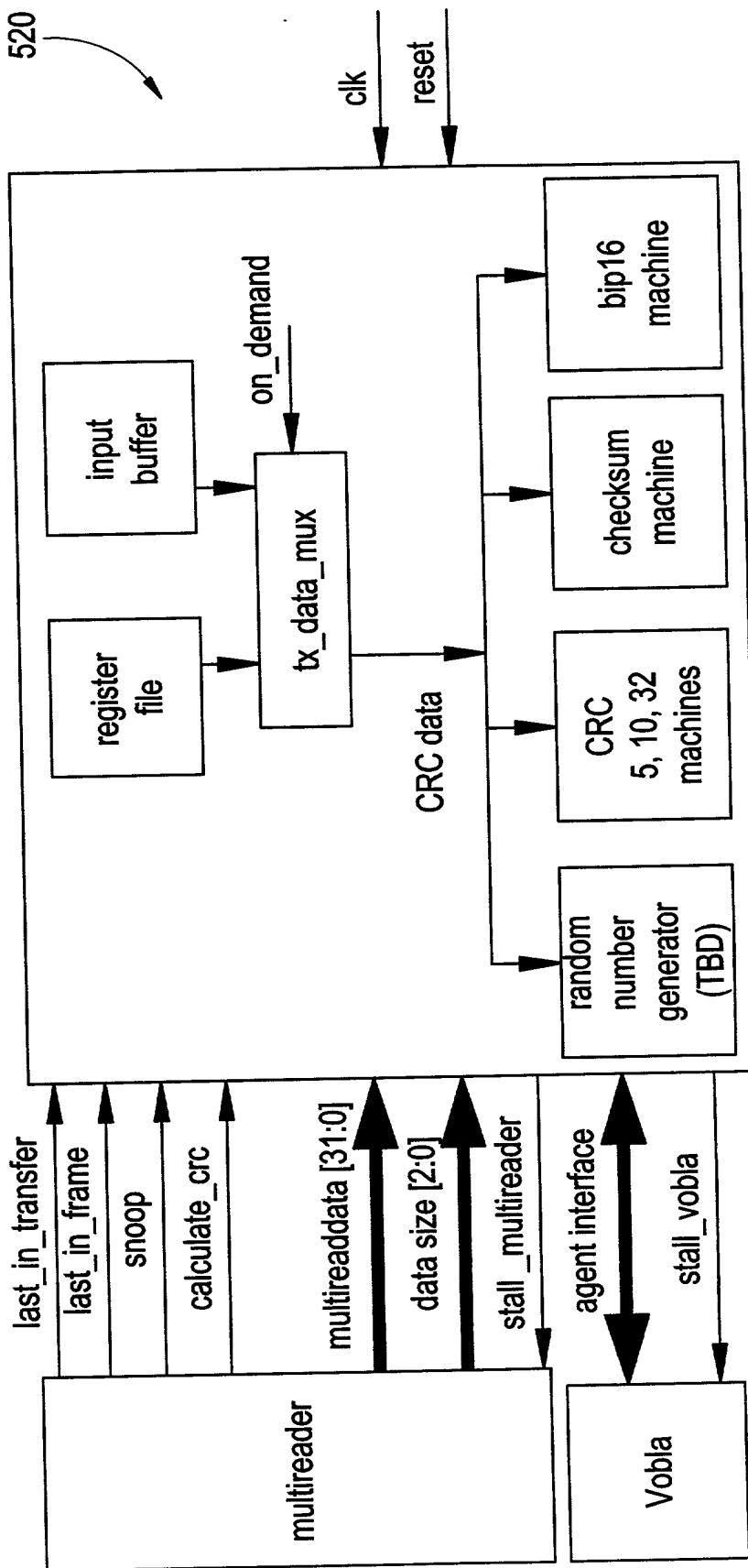


FIG. 56

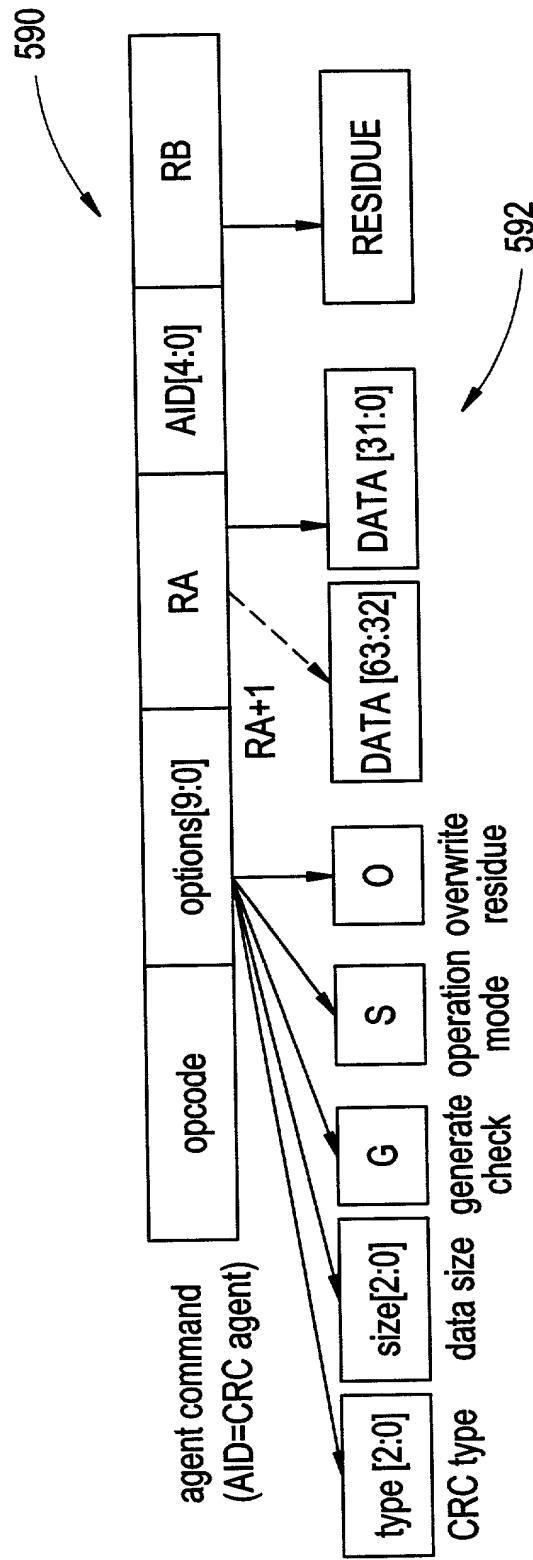


FIG. 57

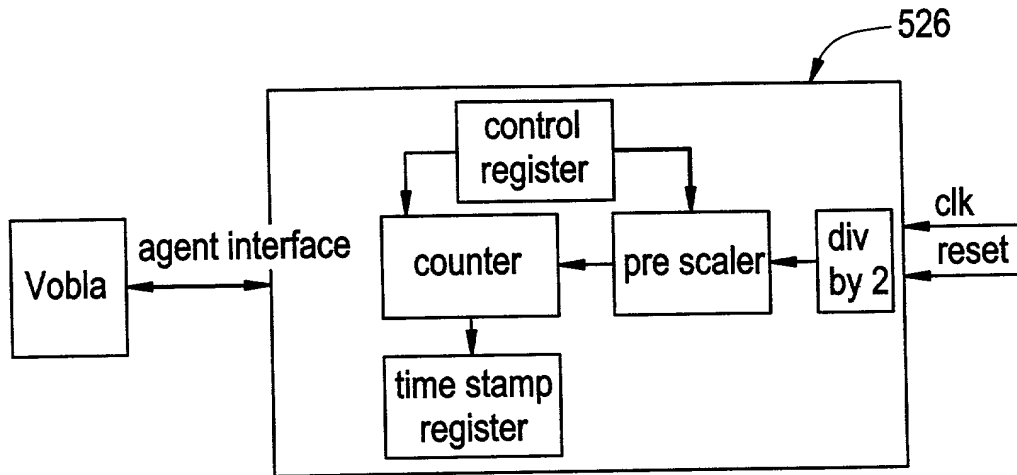


FIG. 58

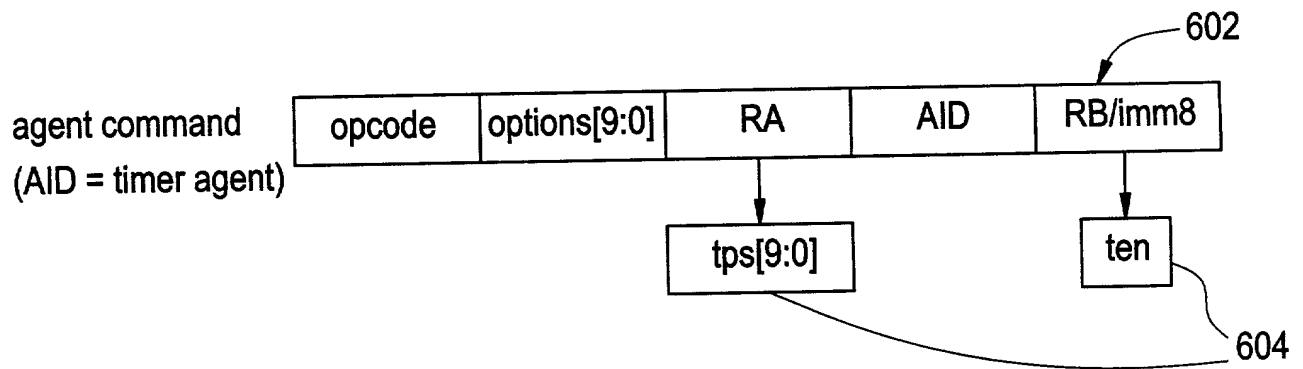


FIG. 59

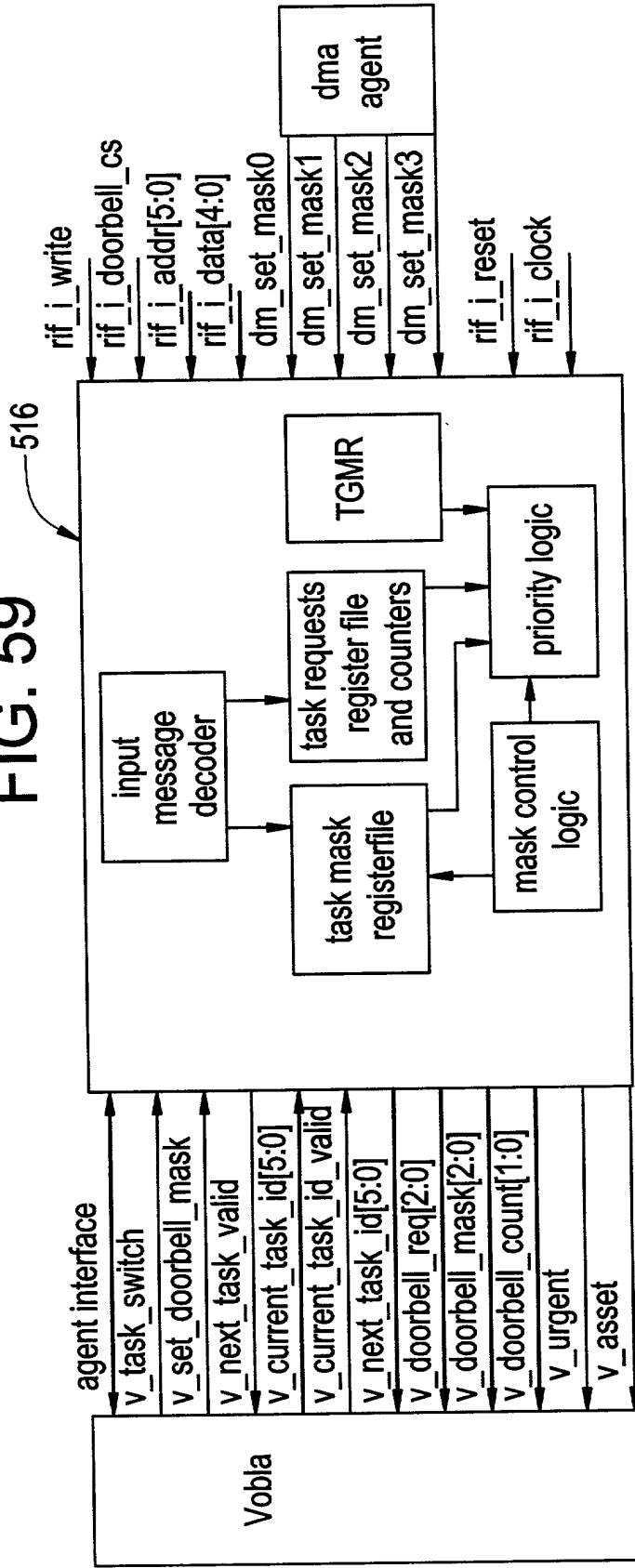


FIG. 60

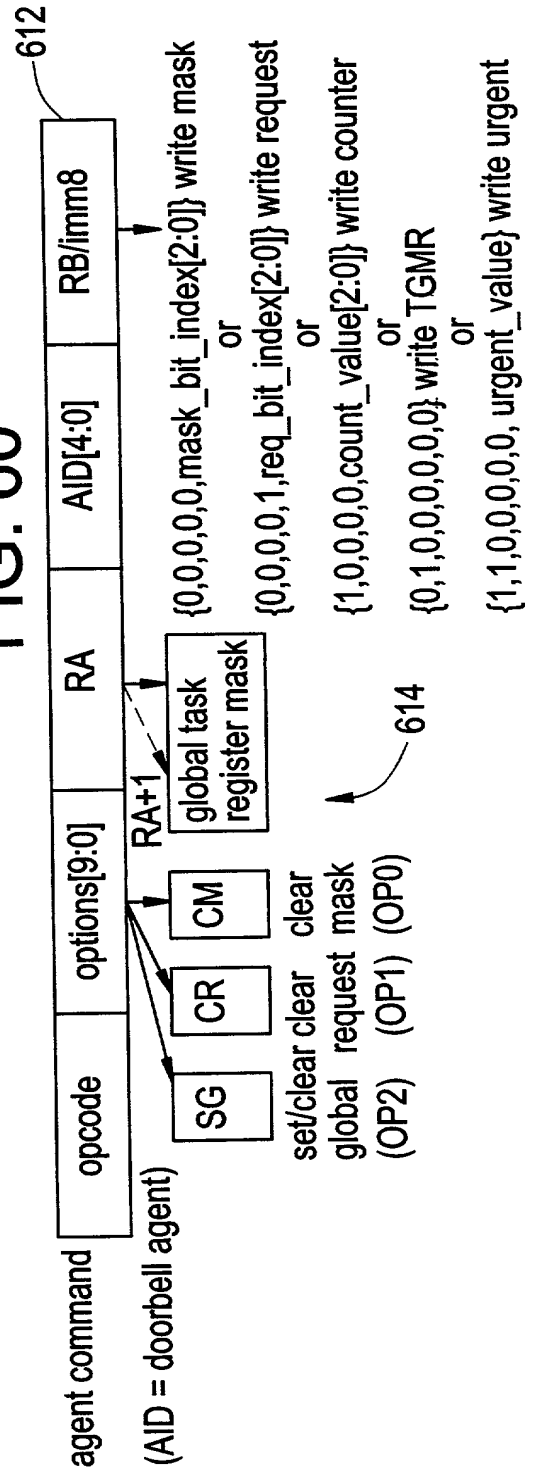


FIG. 61

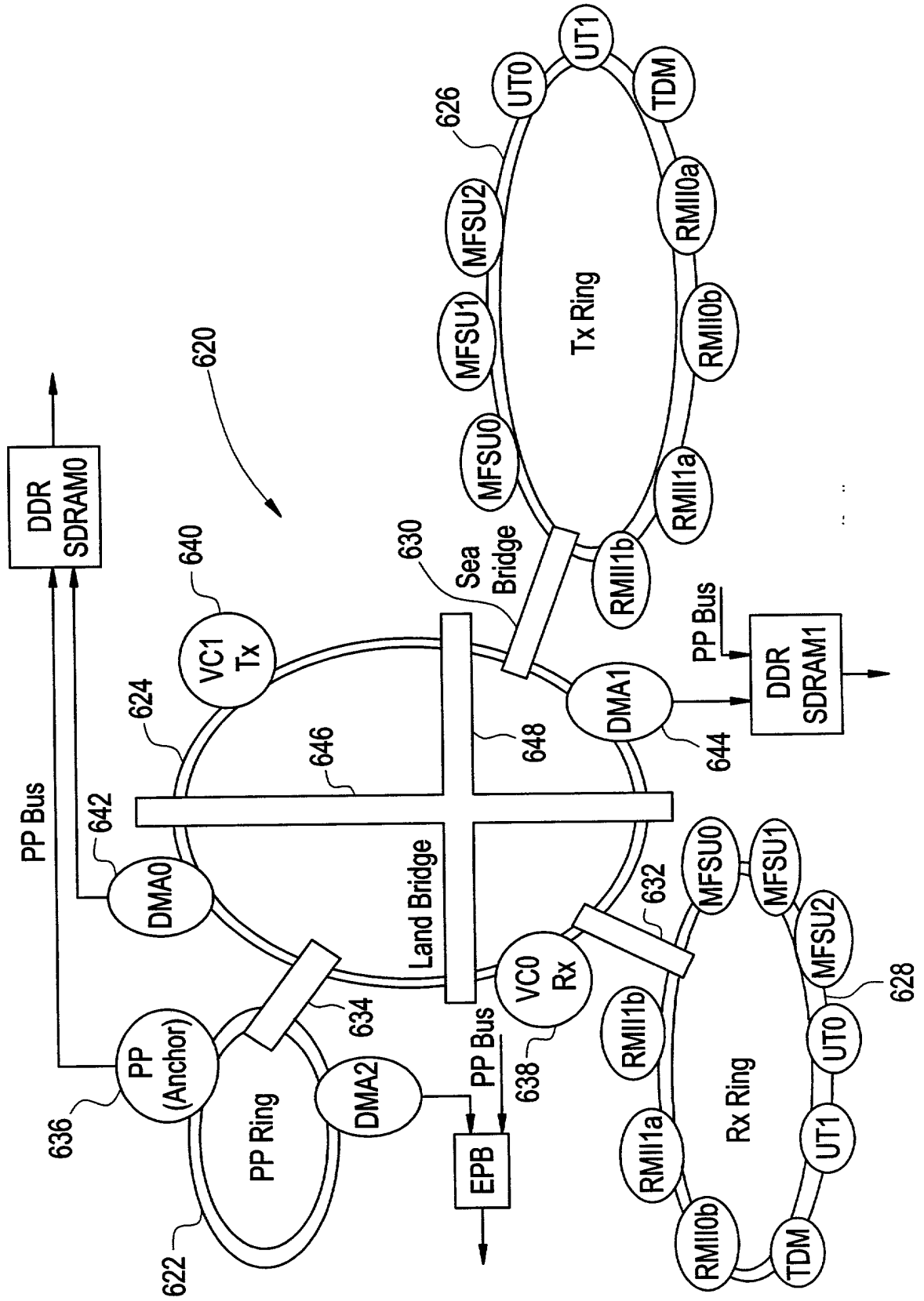


FIG. 62

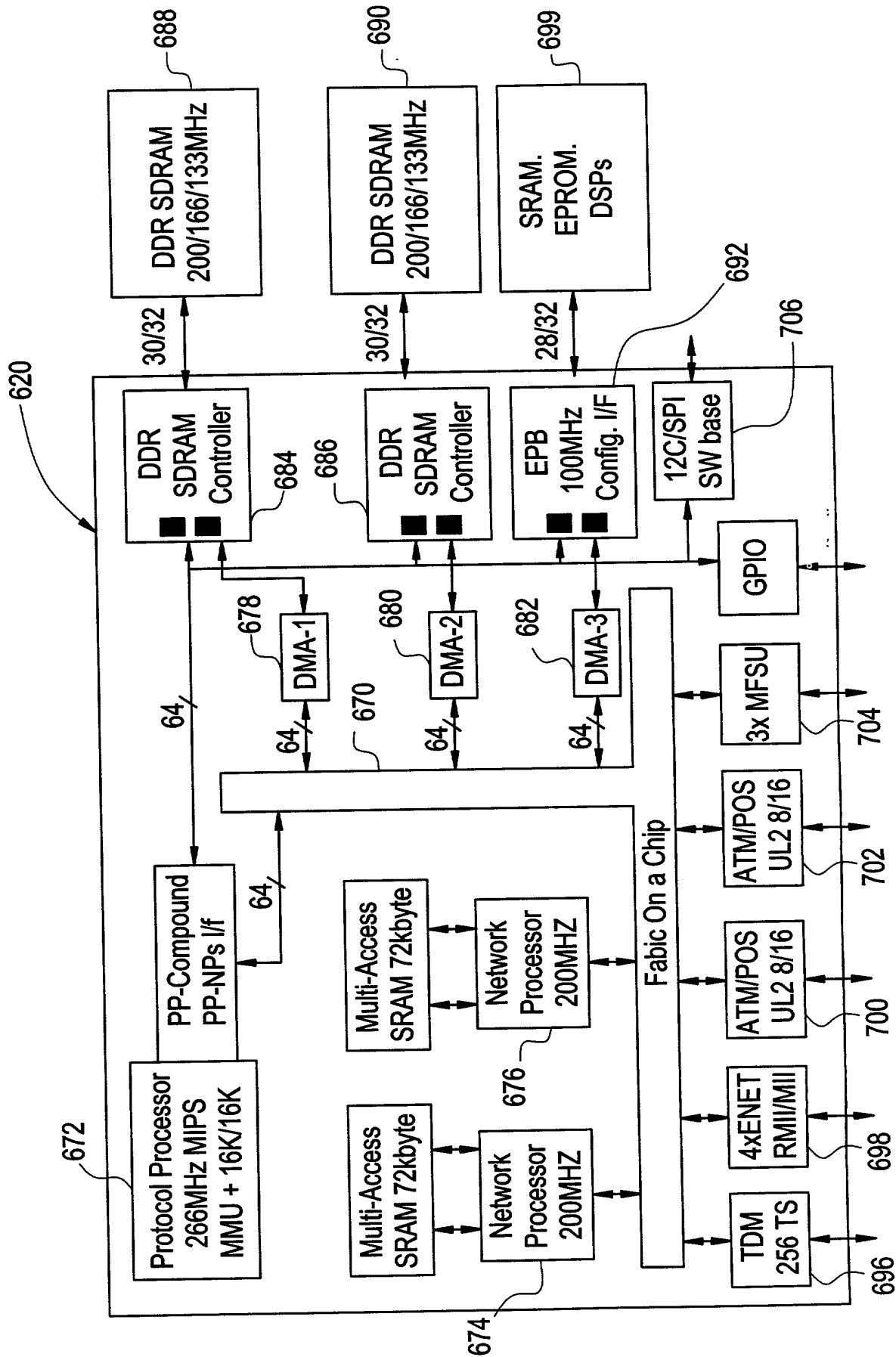


FIG. 63

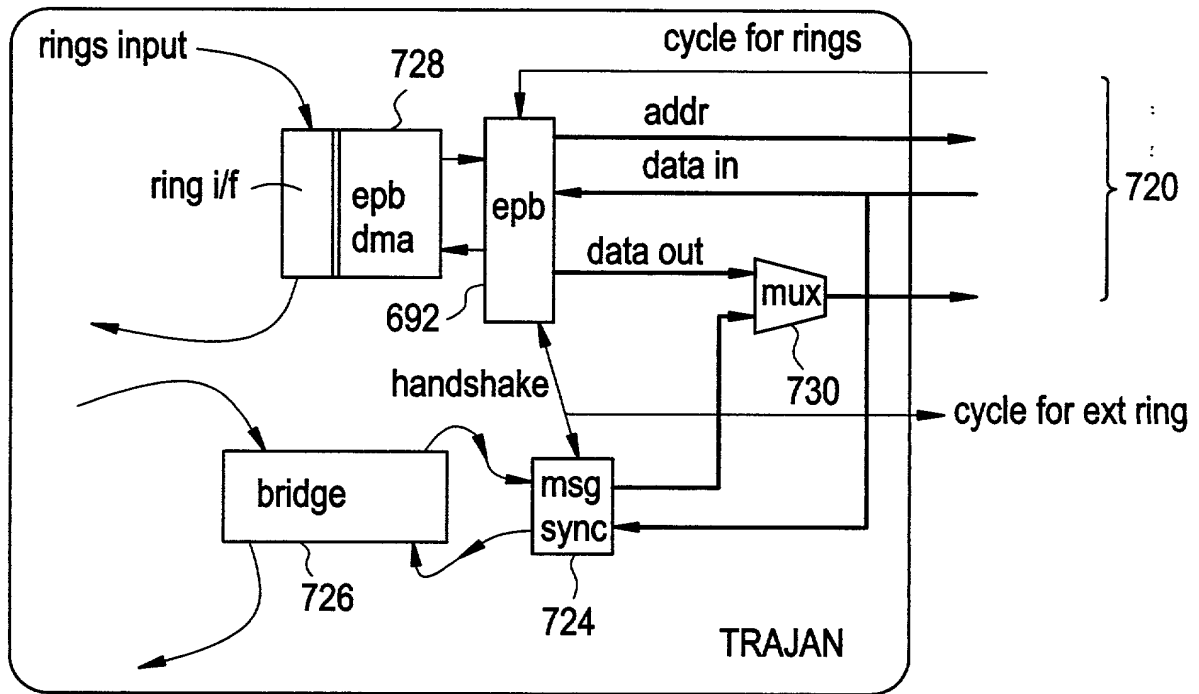


FIG. 64

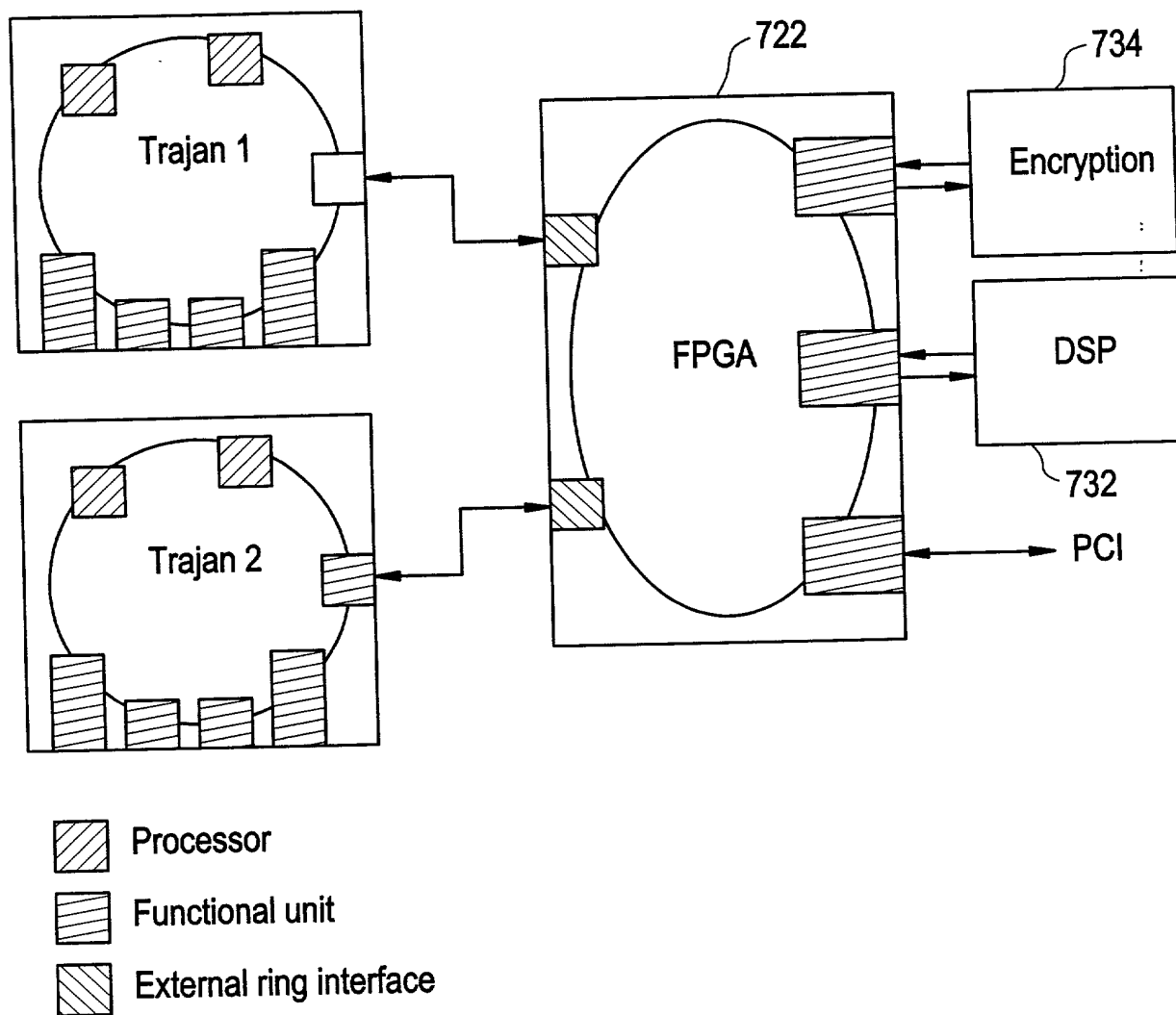




FIG. 65

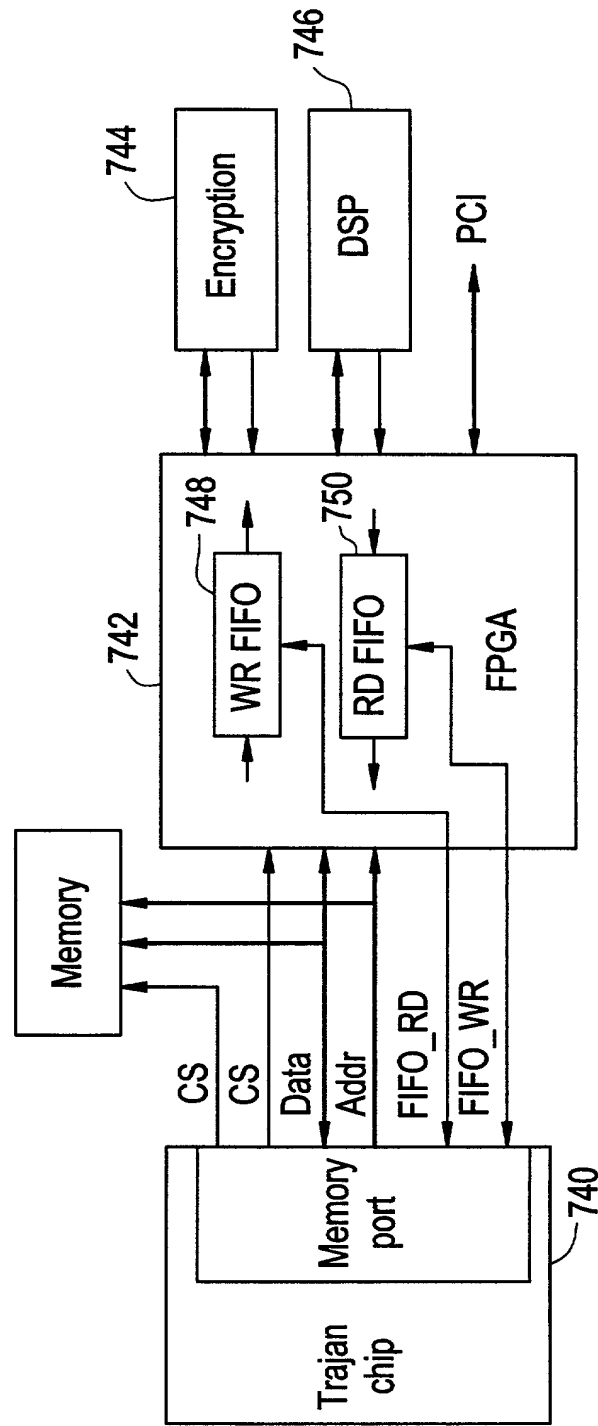


FIG. 66

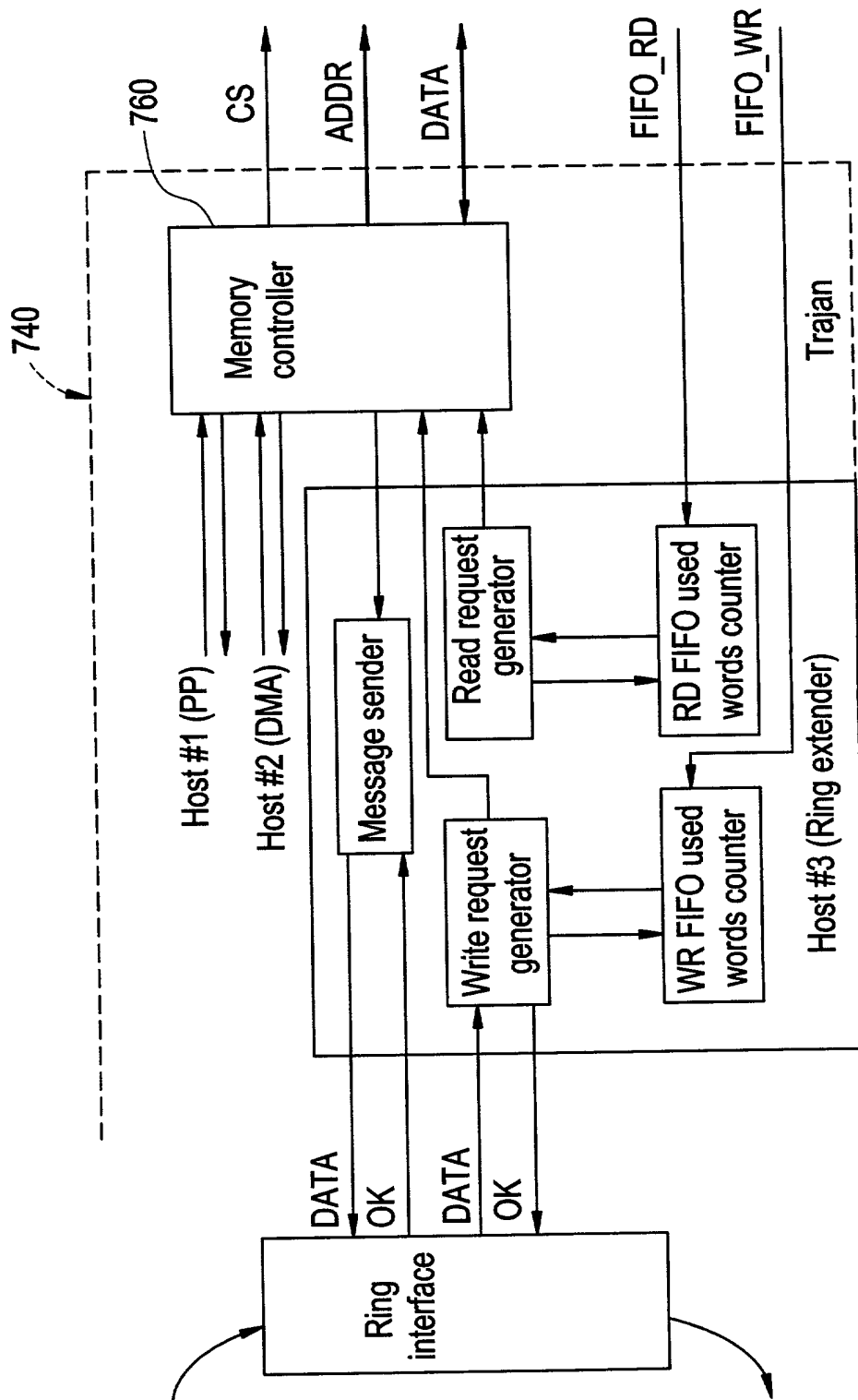
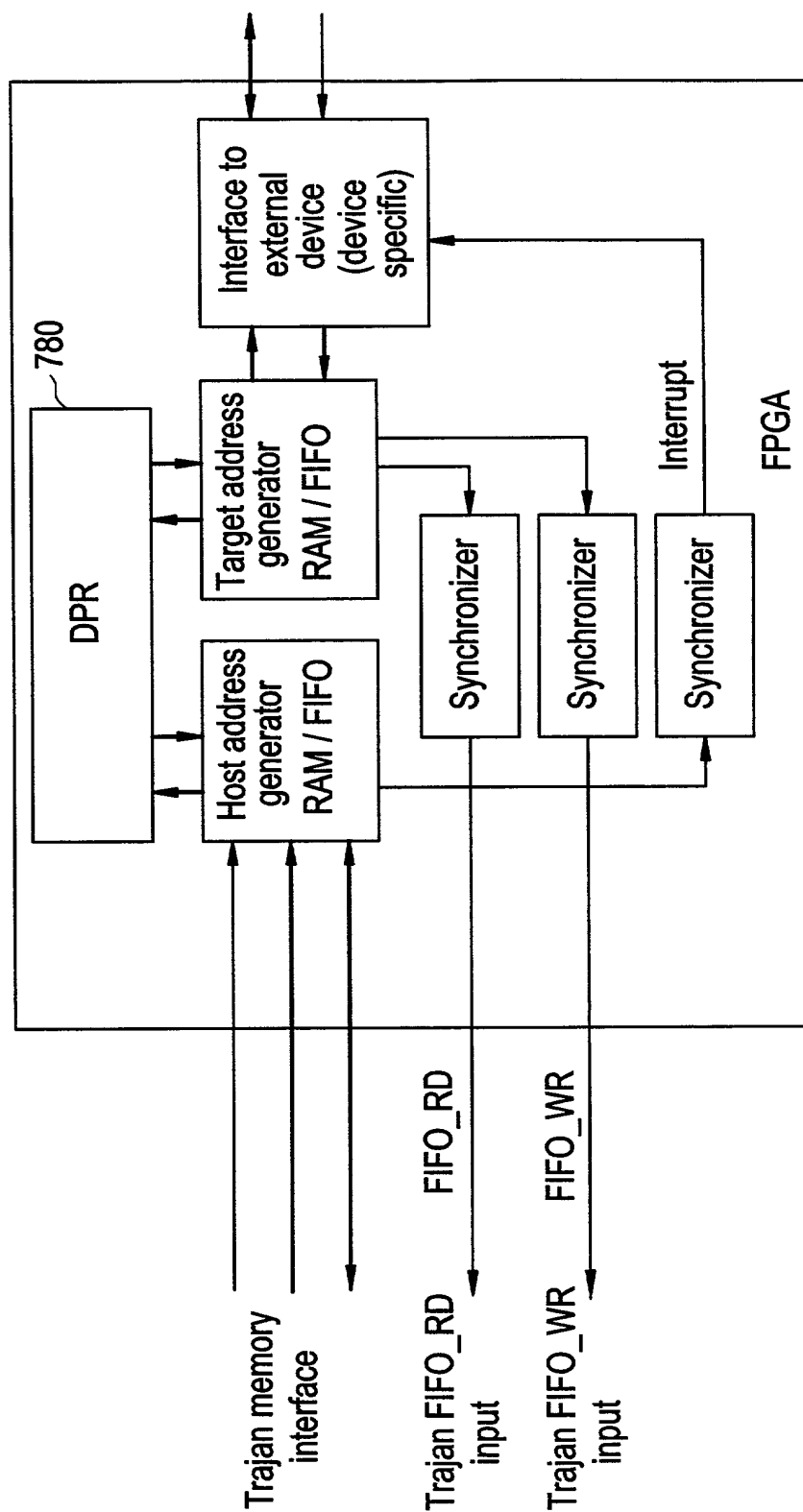


FIG. 67



**FIG. 68**

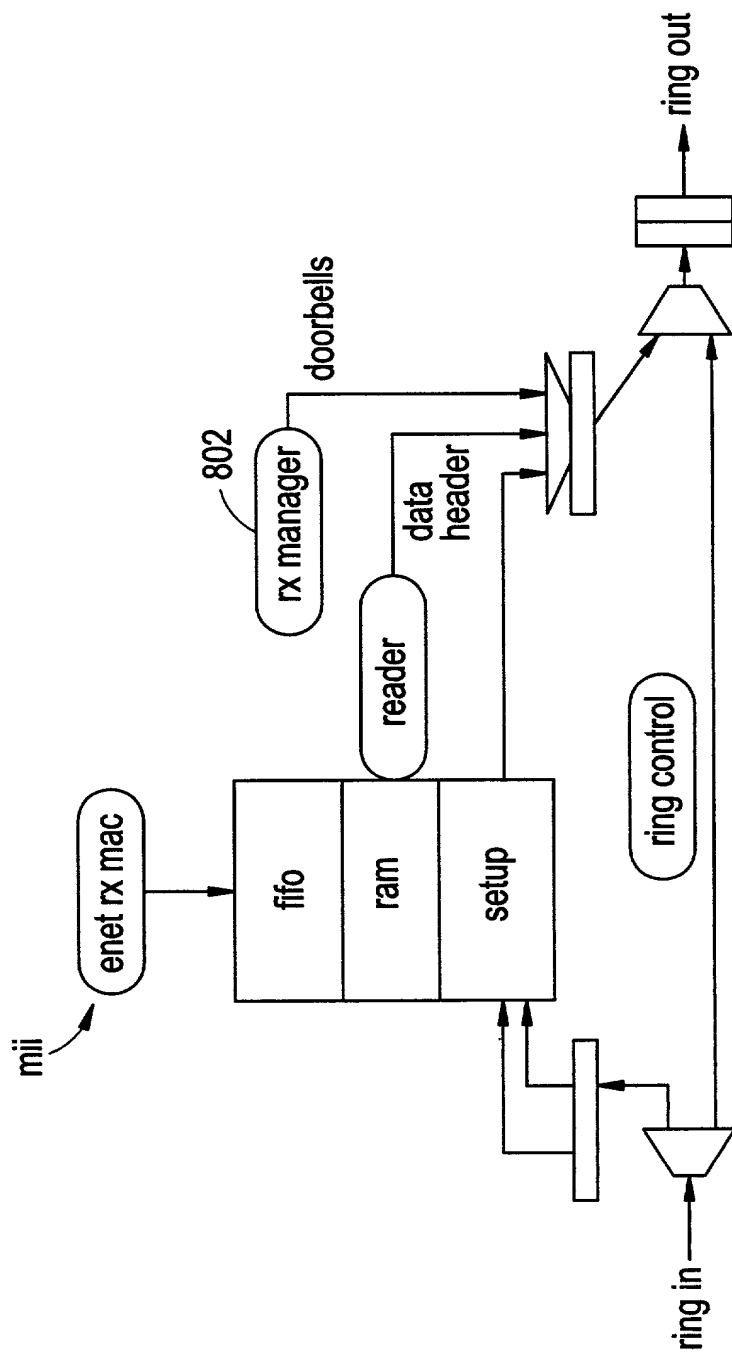


FIG. 69

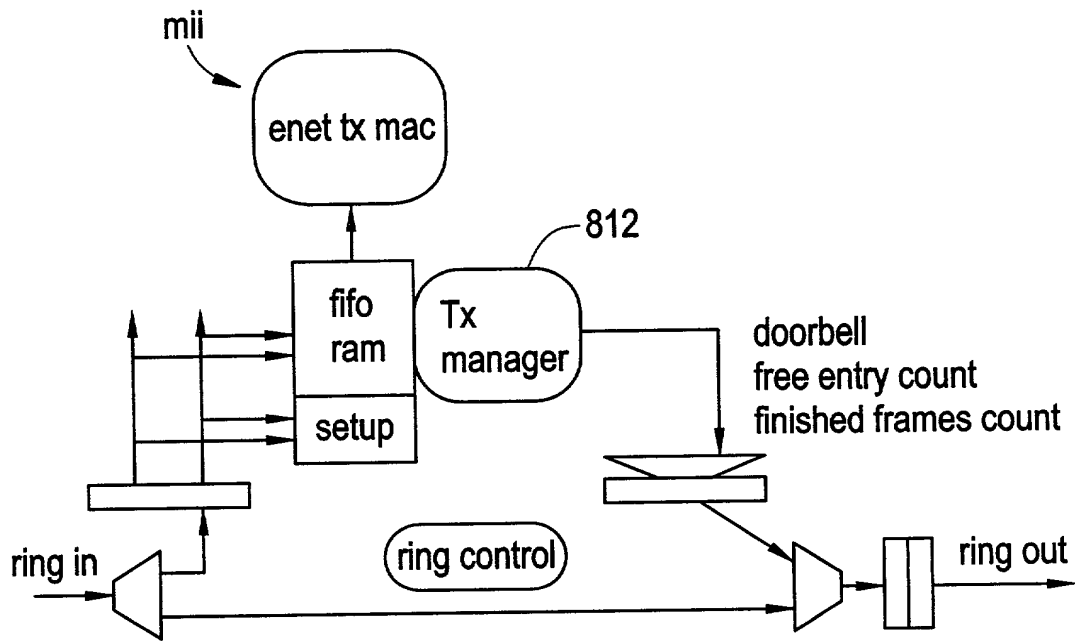
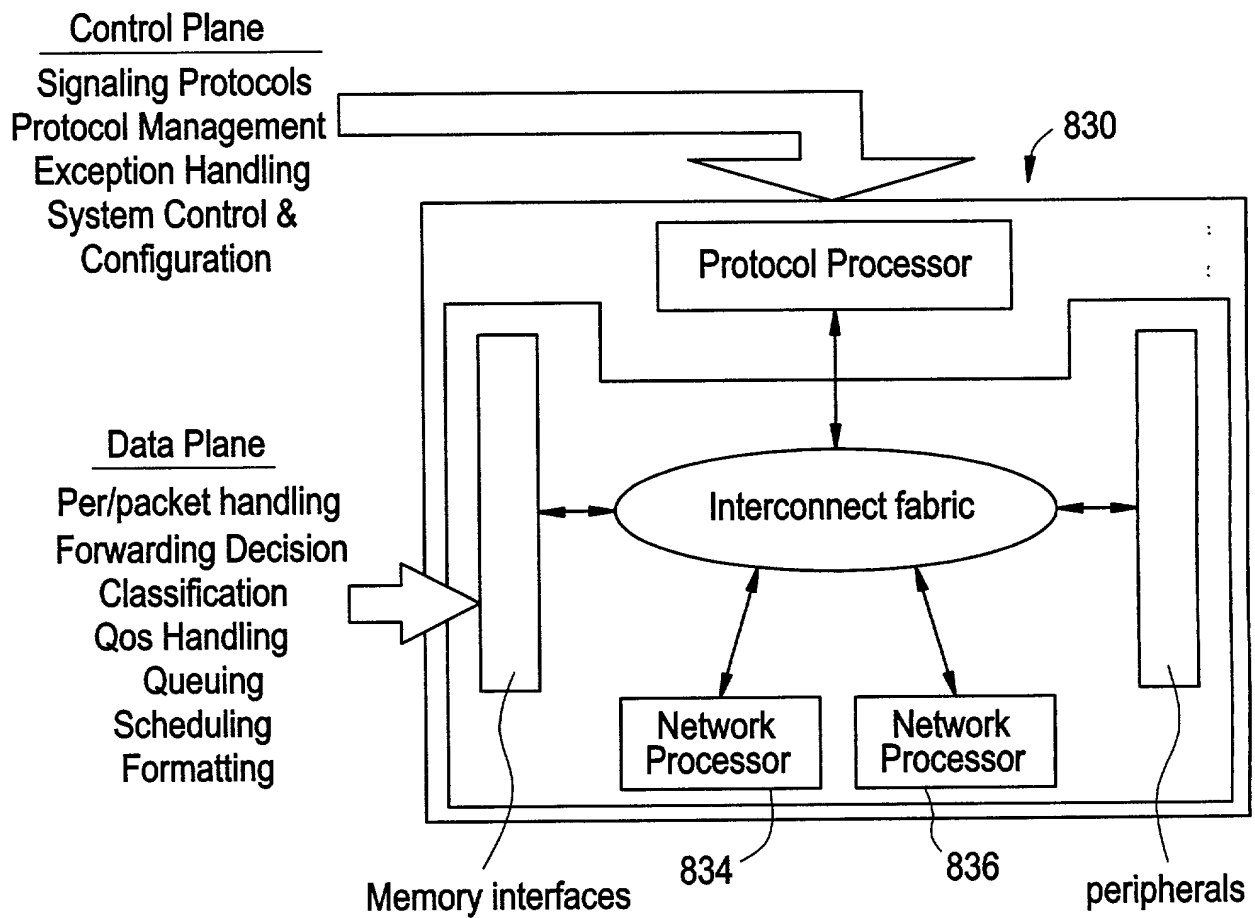


FIG. 70



1006429 09360

FIG. 71

840

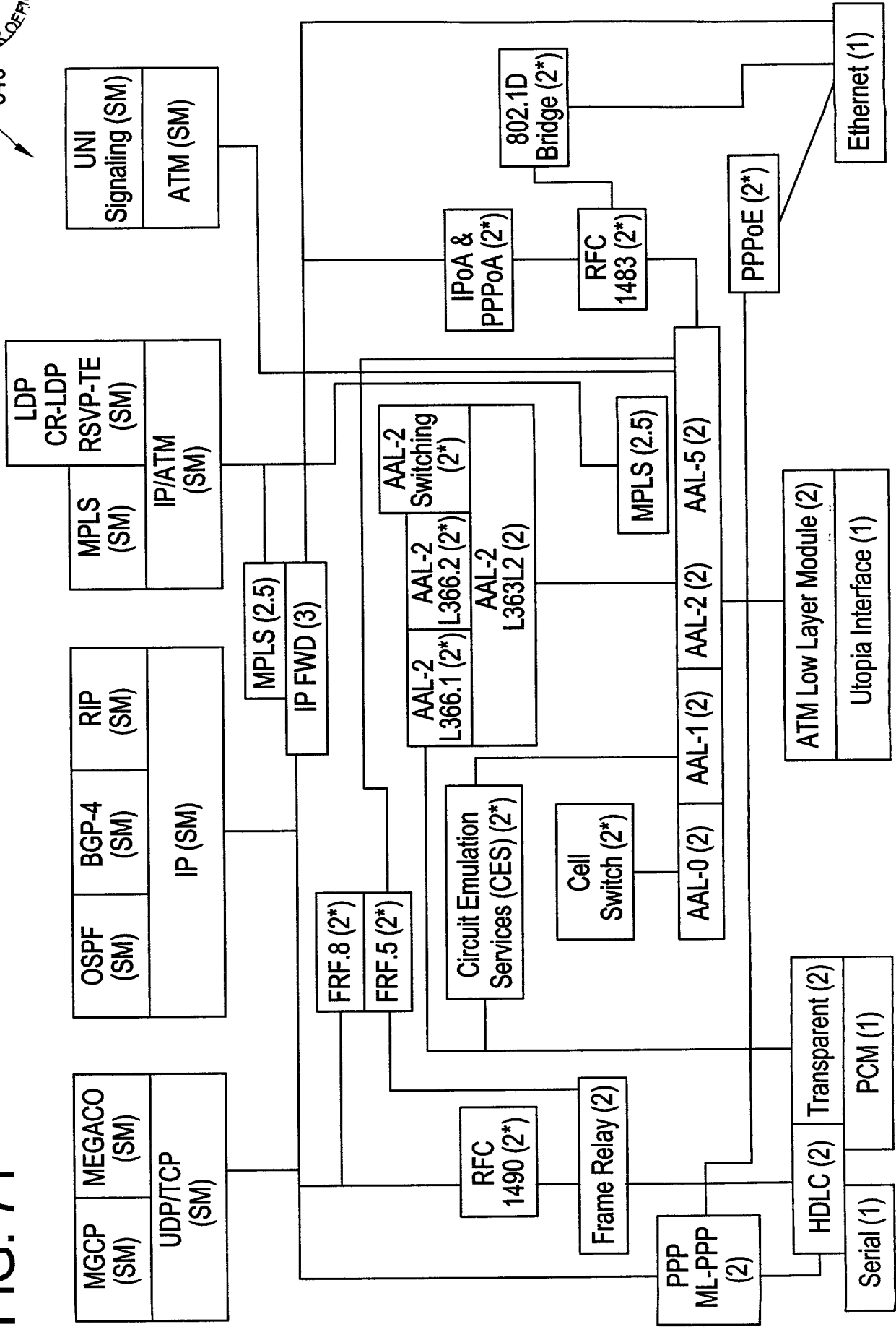
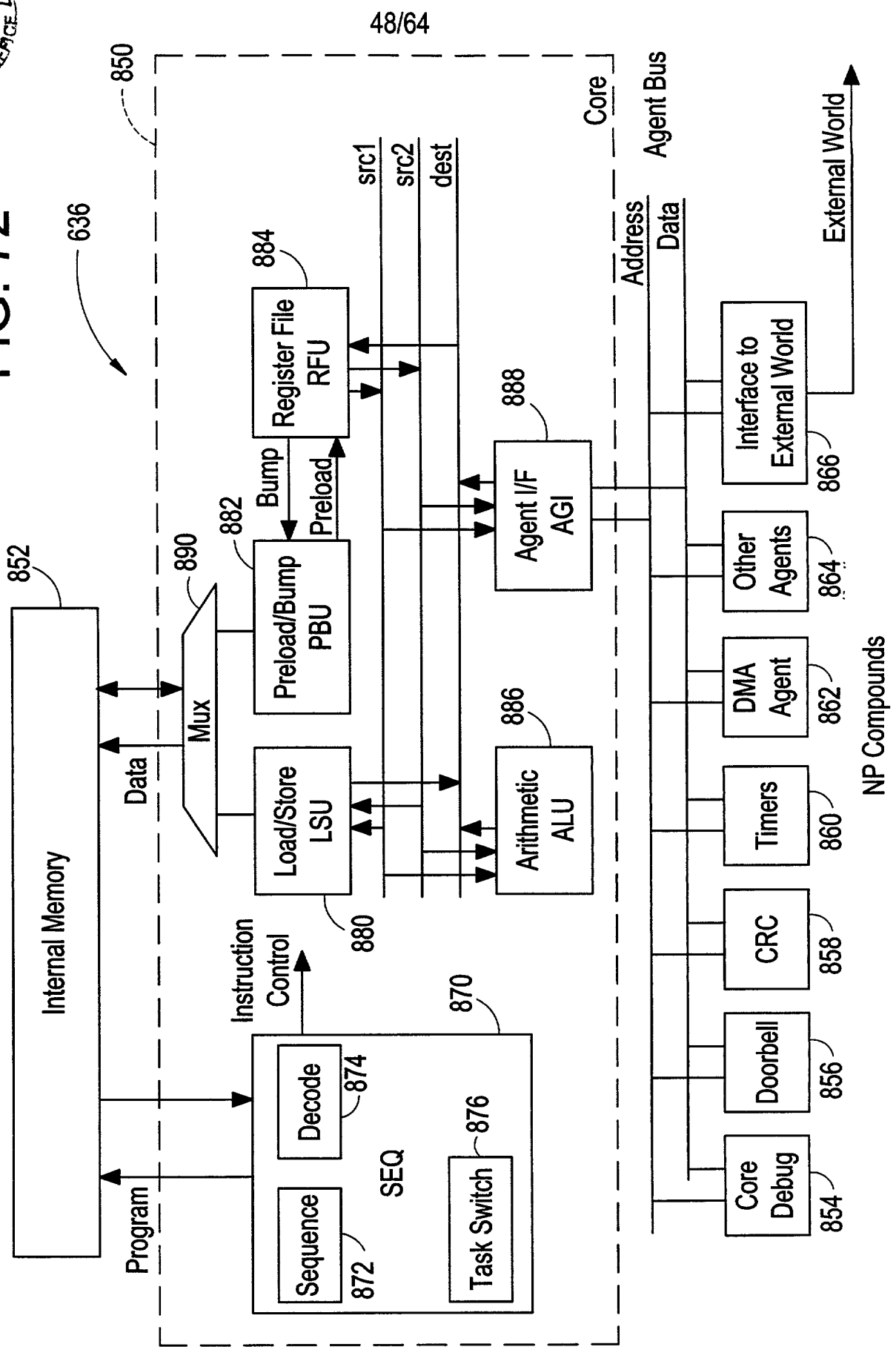


FIG. 72



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FIG. 73

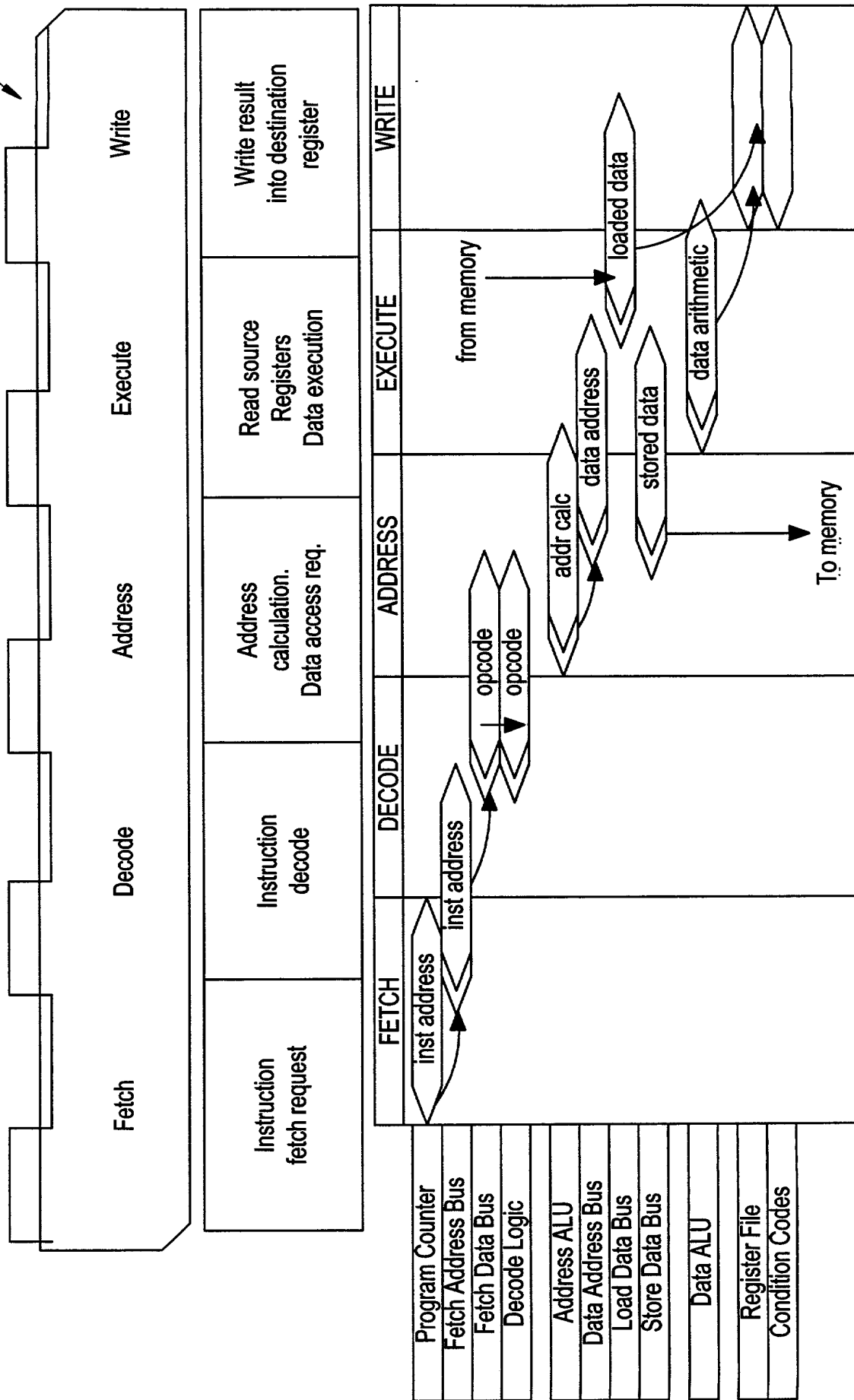


FIG. 74

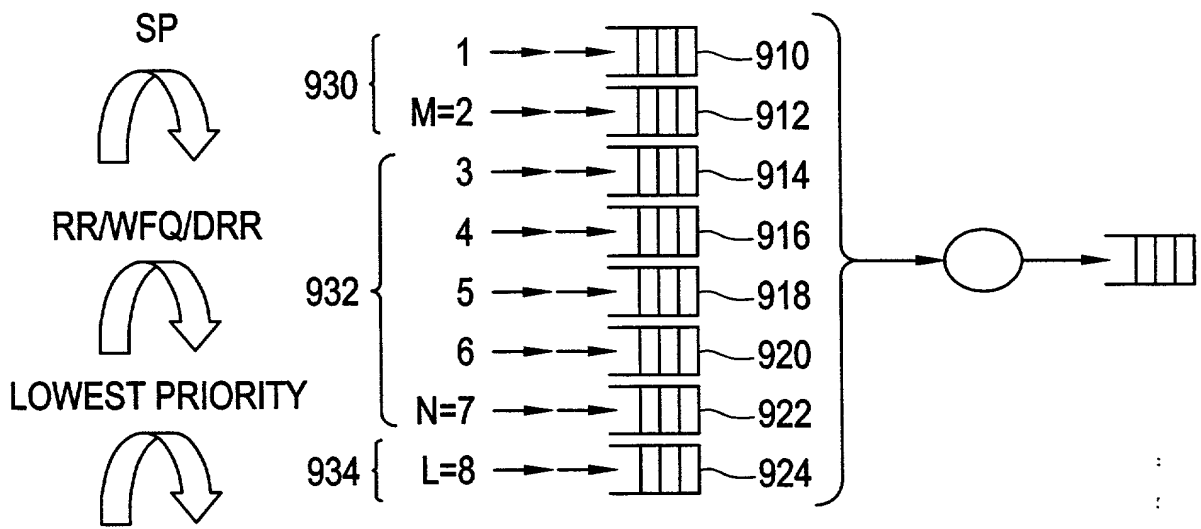


FIG. 75

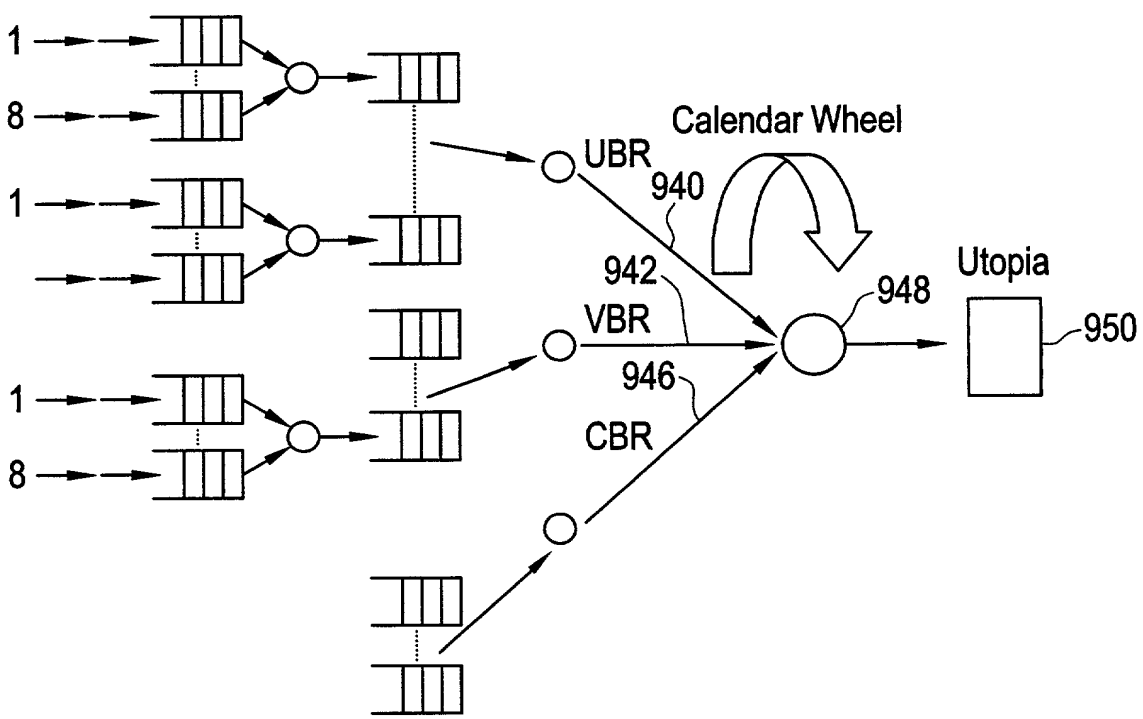


FIG. 76

960

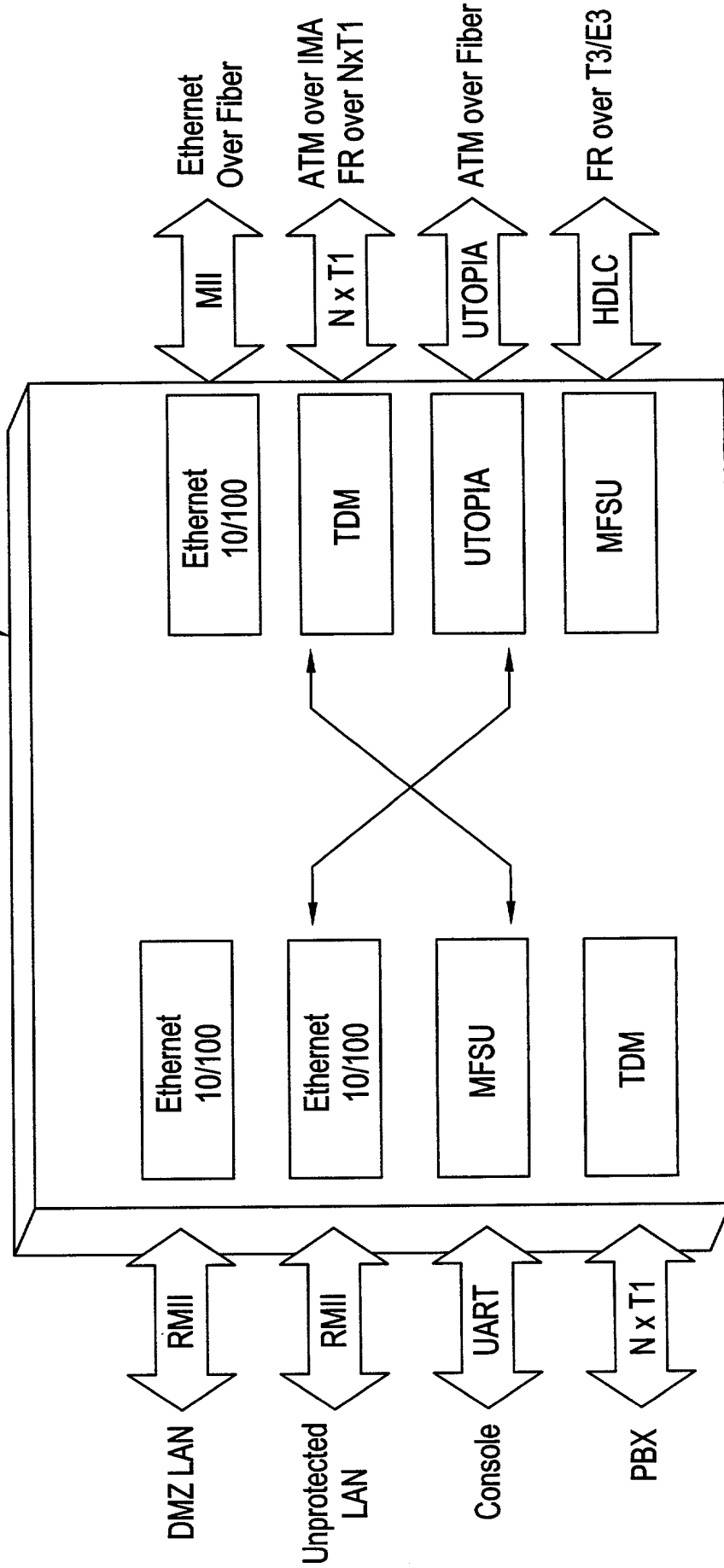


FIG. 77

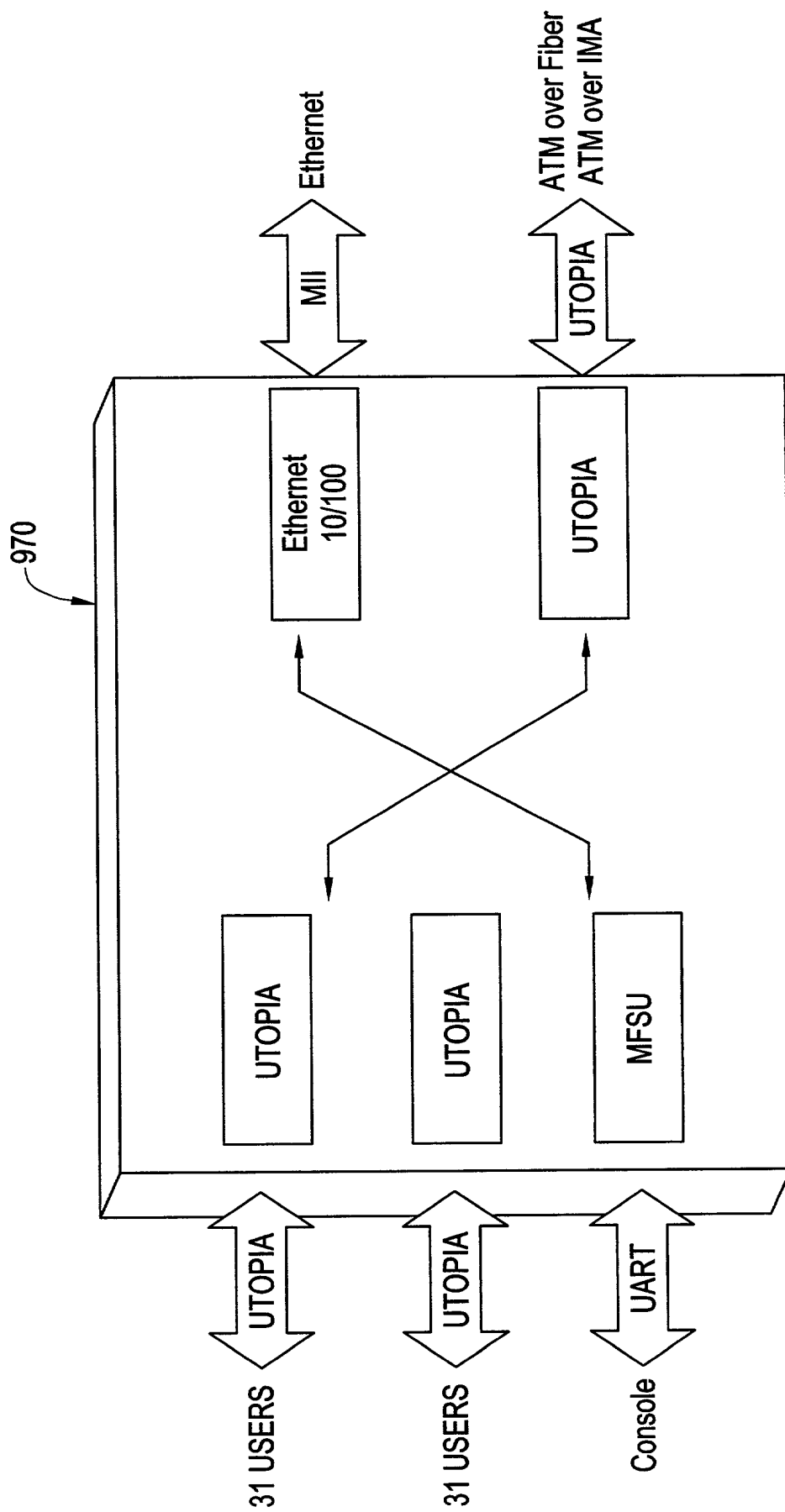


FIG. 78

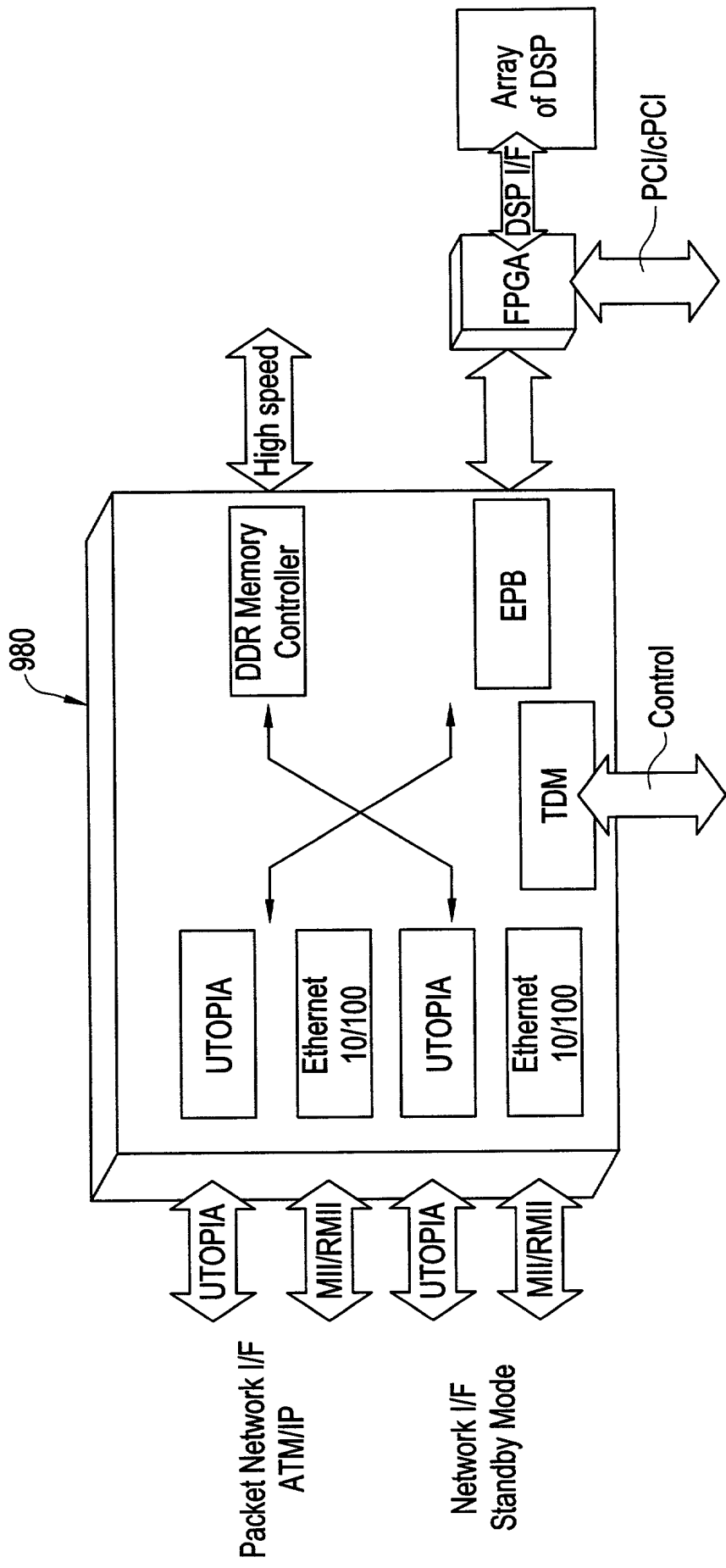


FIG. 79 is a block diagram of a radio card system 990.

FIG. 79

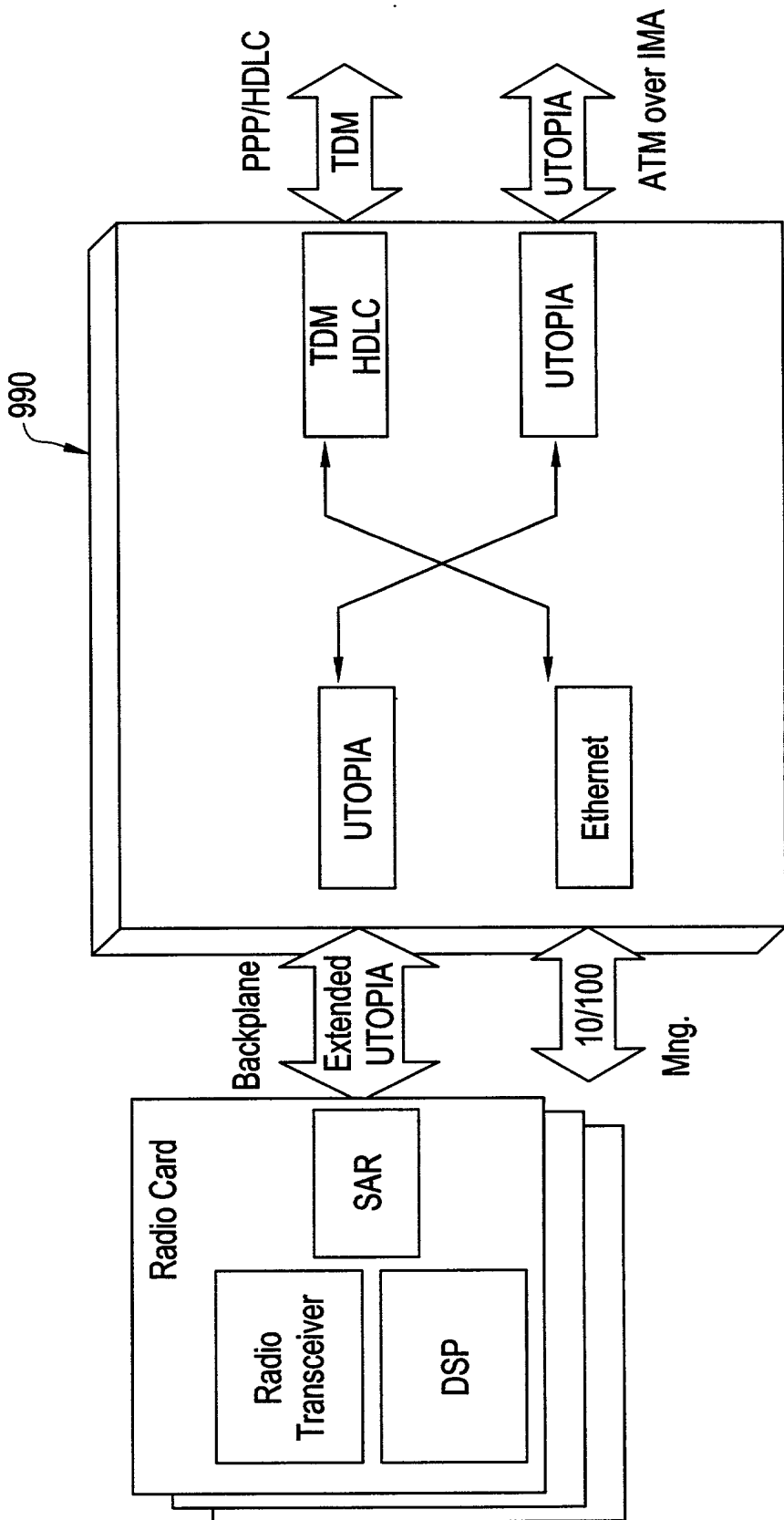
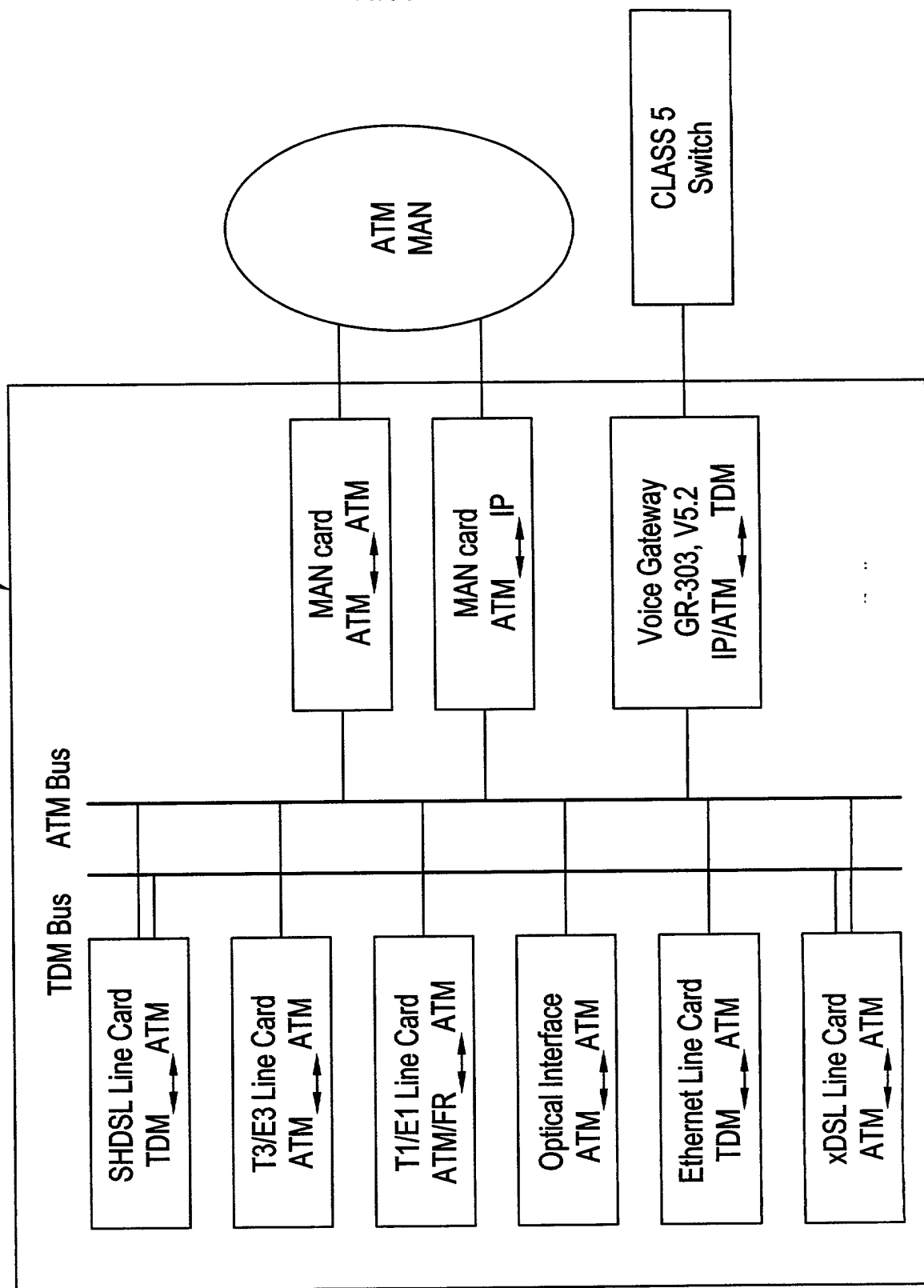


FIG. 80

1000



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20090901 06264900T

FIG. 81

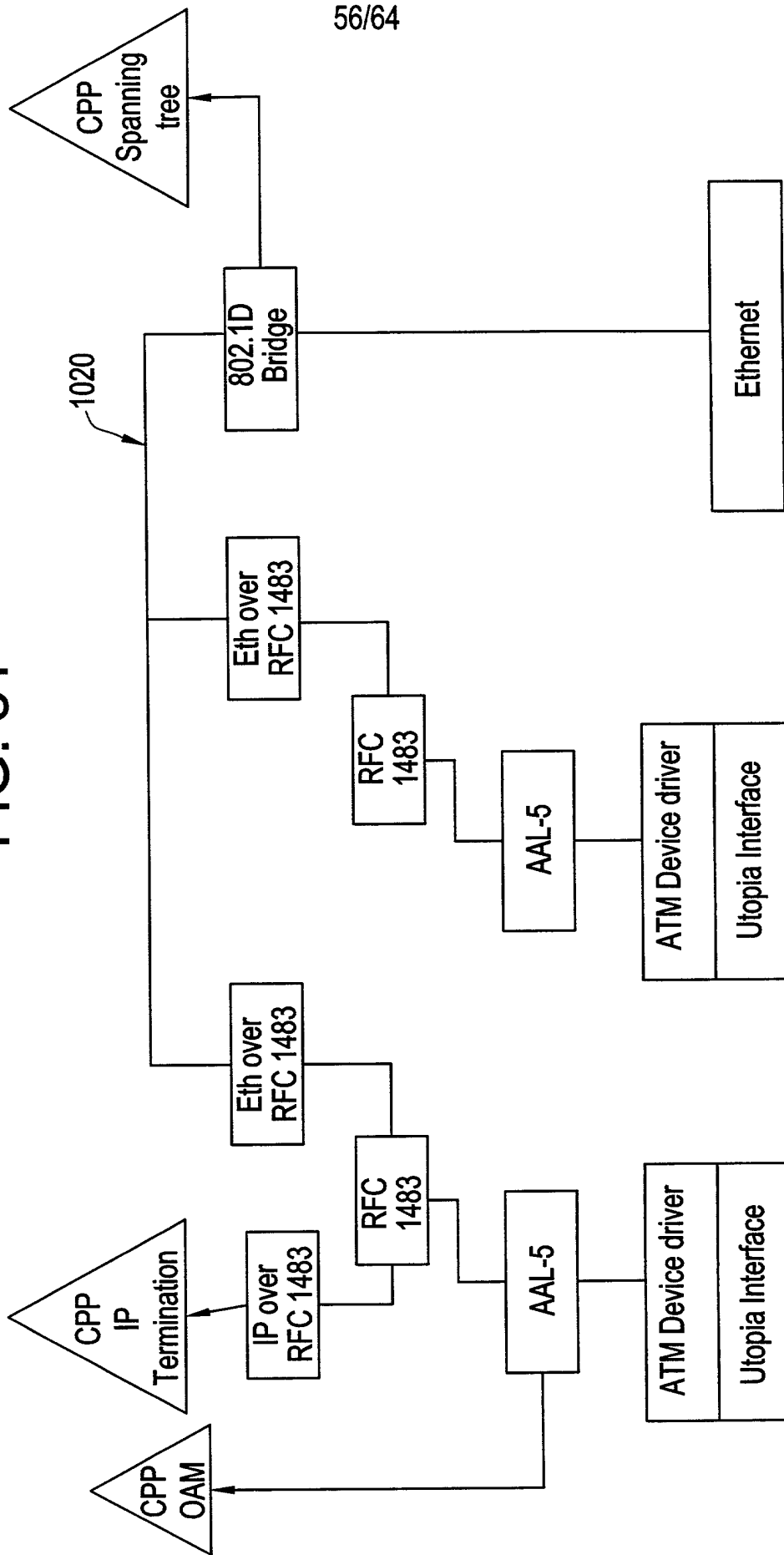




FIG. 82

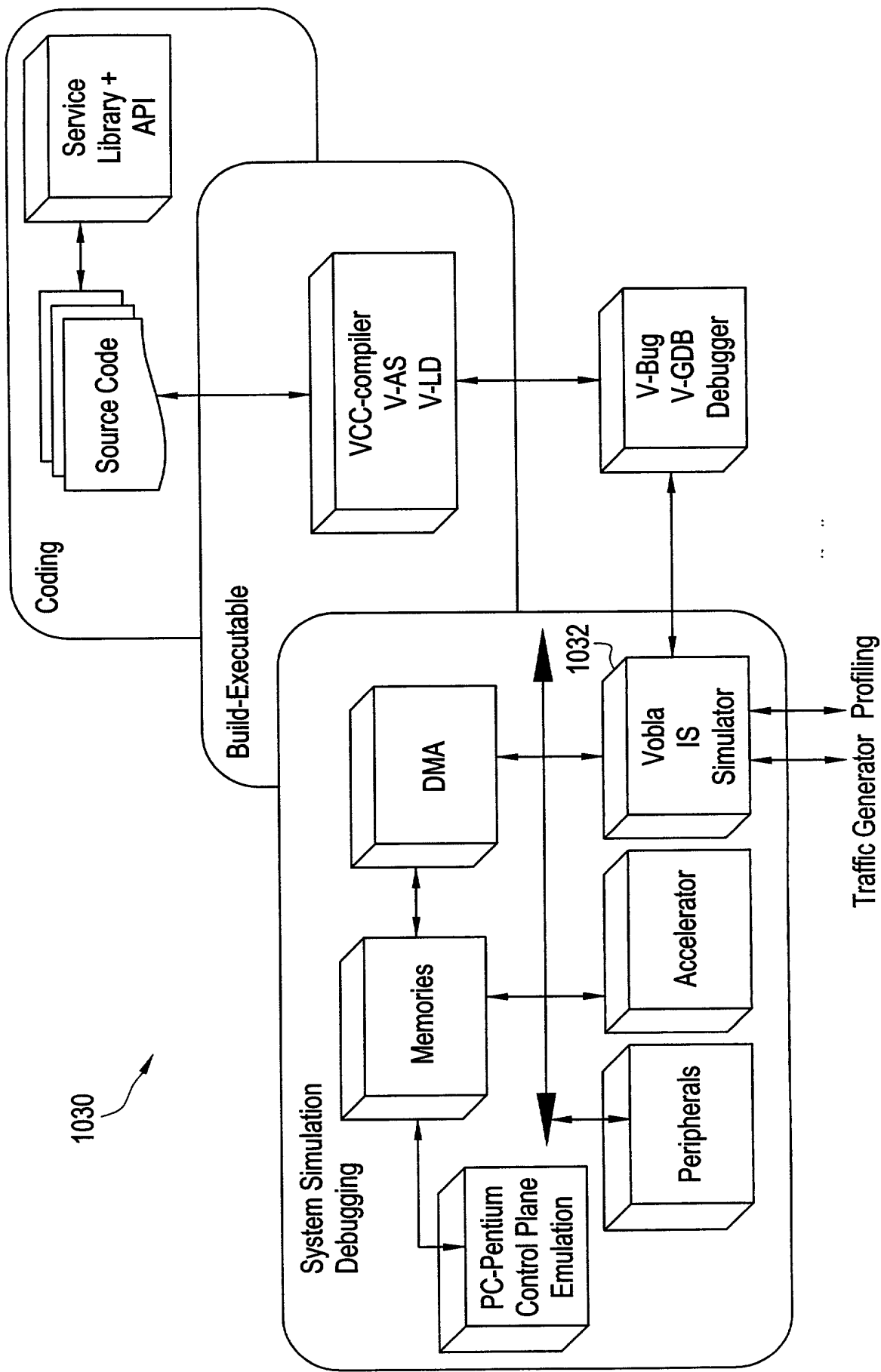


FIG. 83

1040

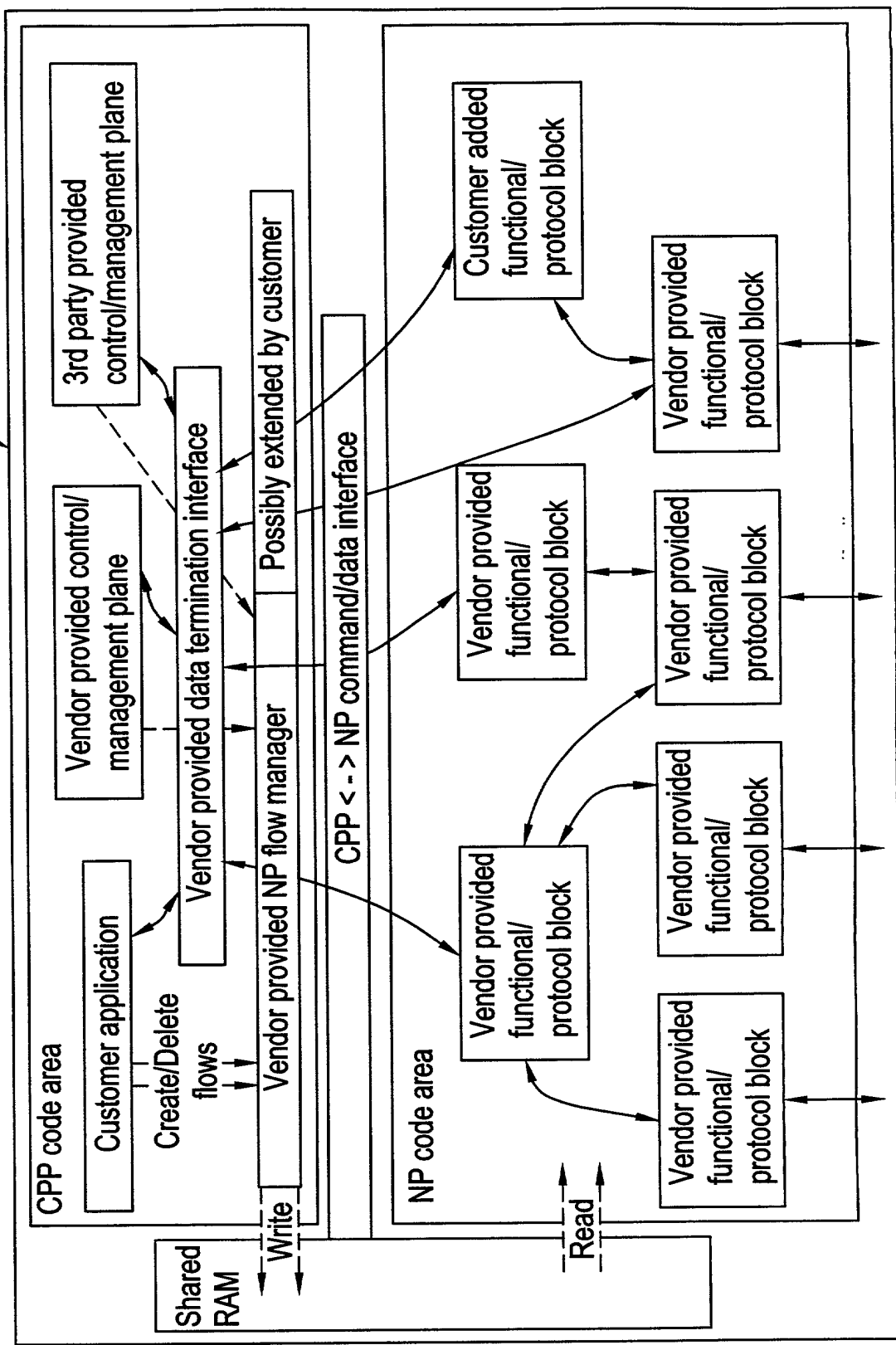


FIG. 84

1050

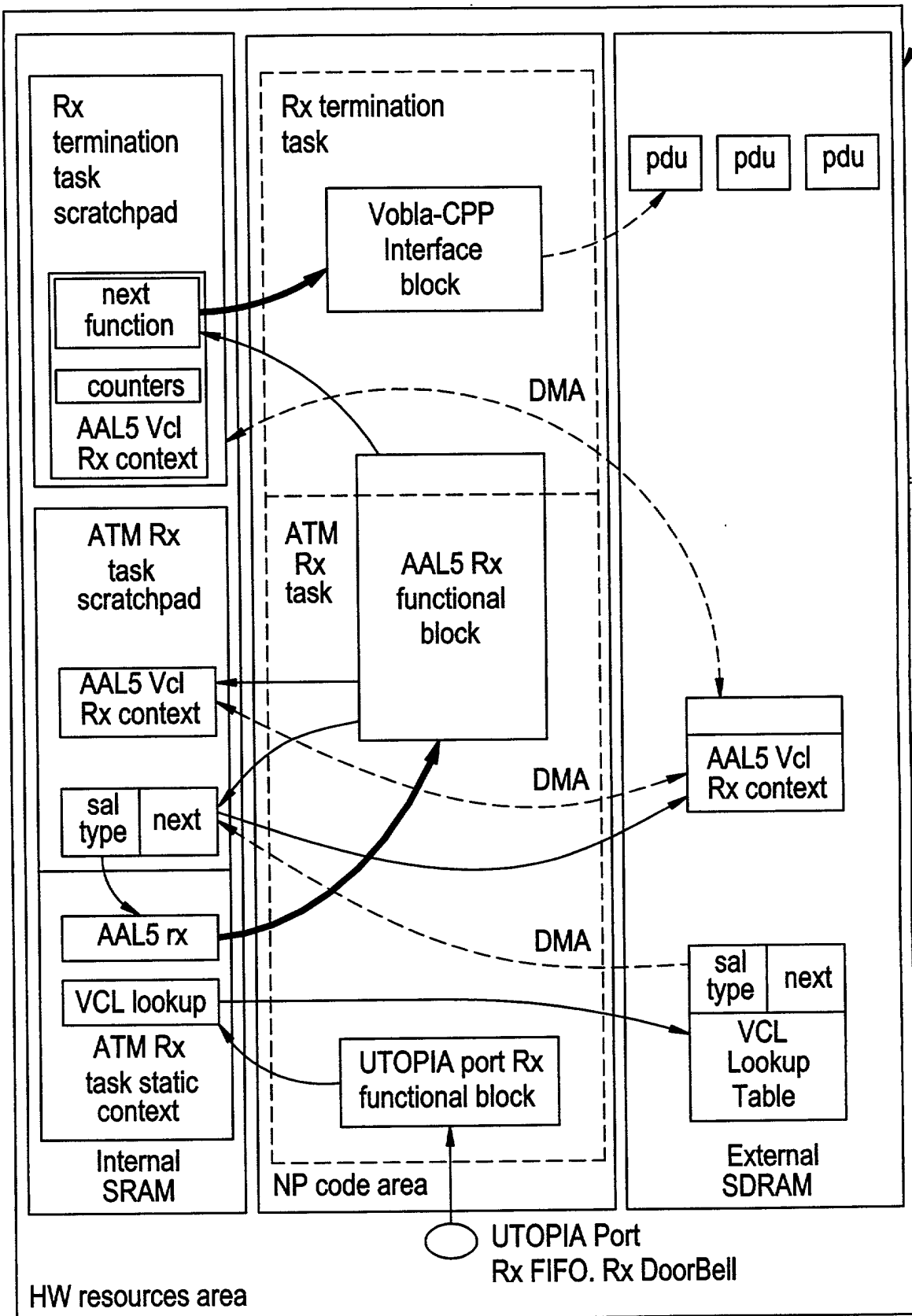


FIG. 85

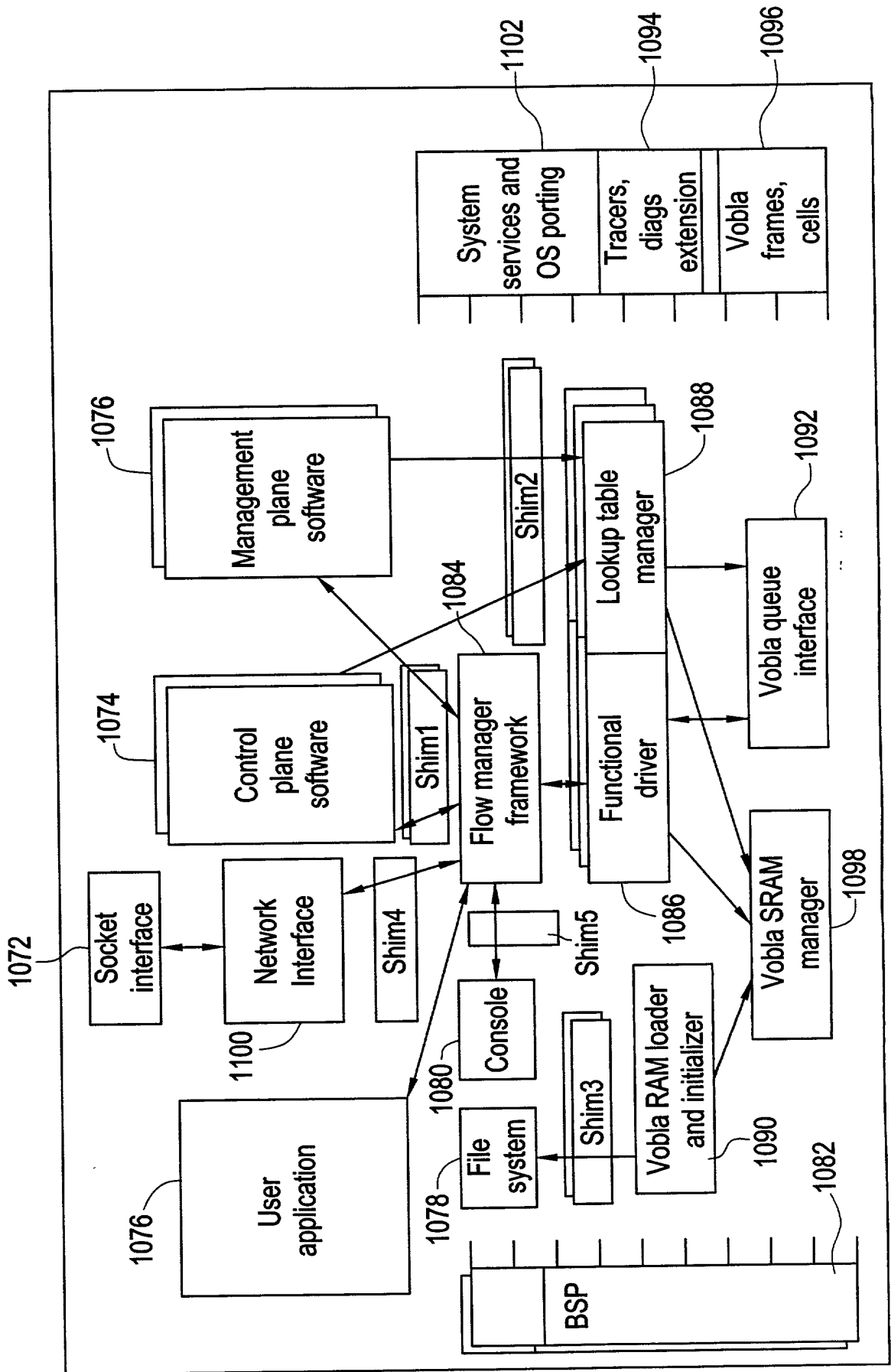
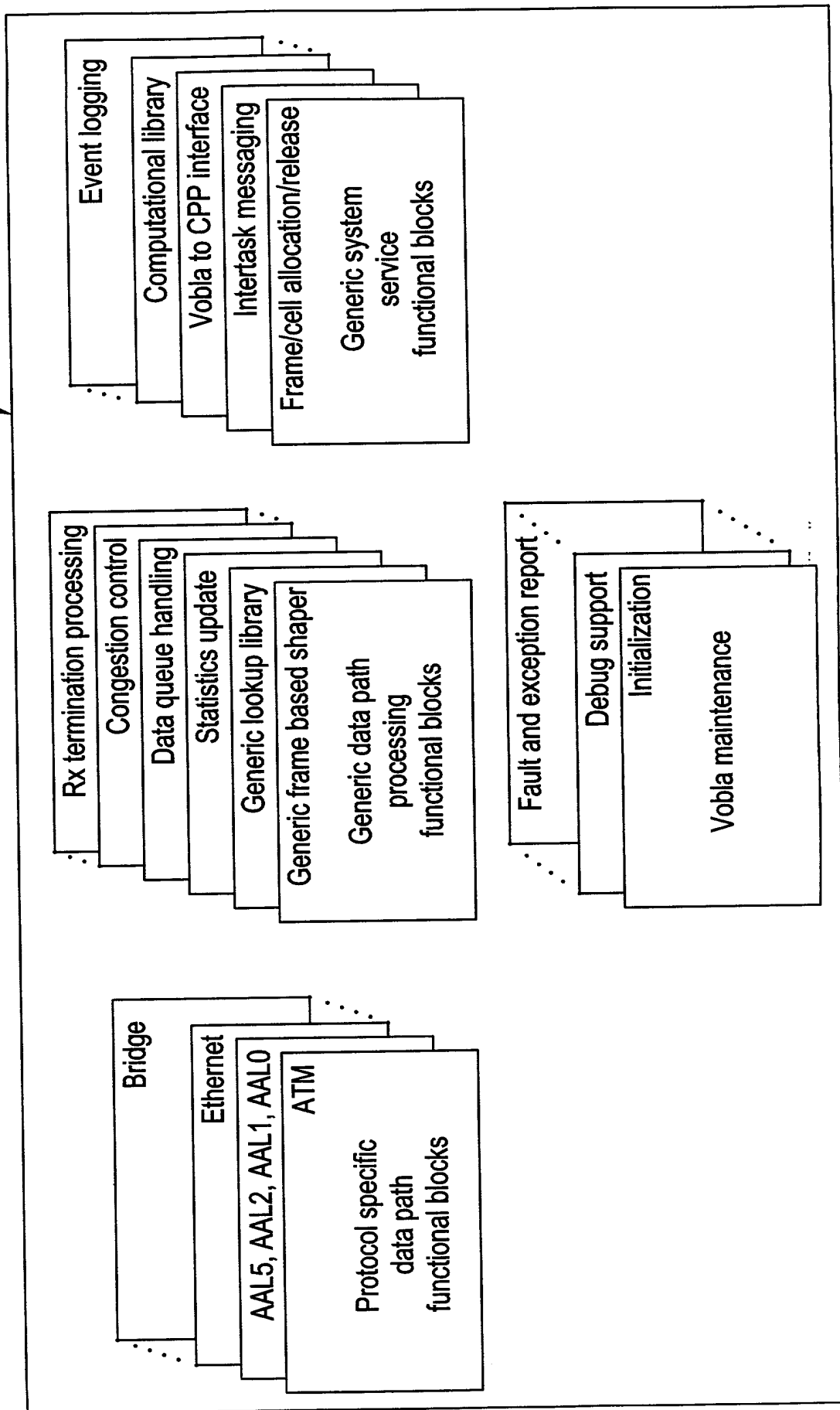


FIG. 86

1200

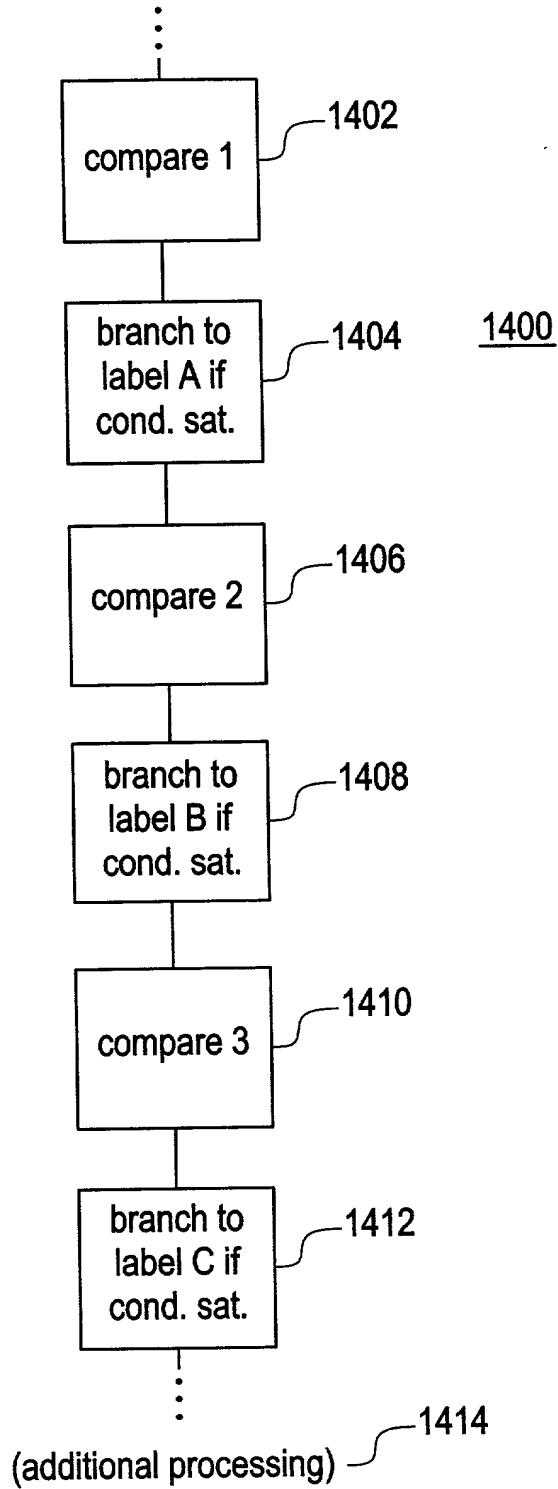




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# FIG. 87

PRIOR ART



“62/64” sheet

FIG. 88

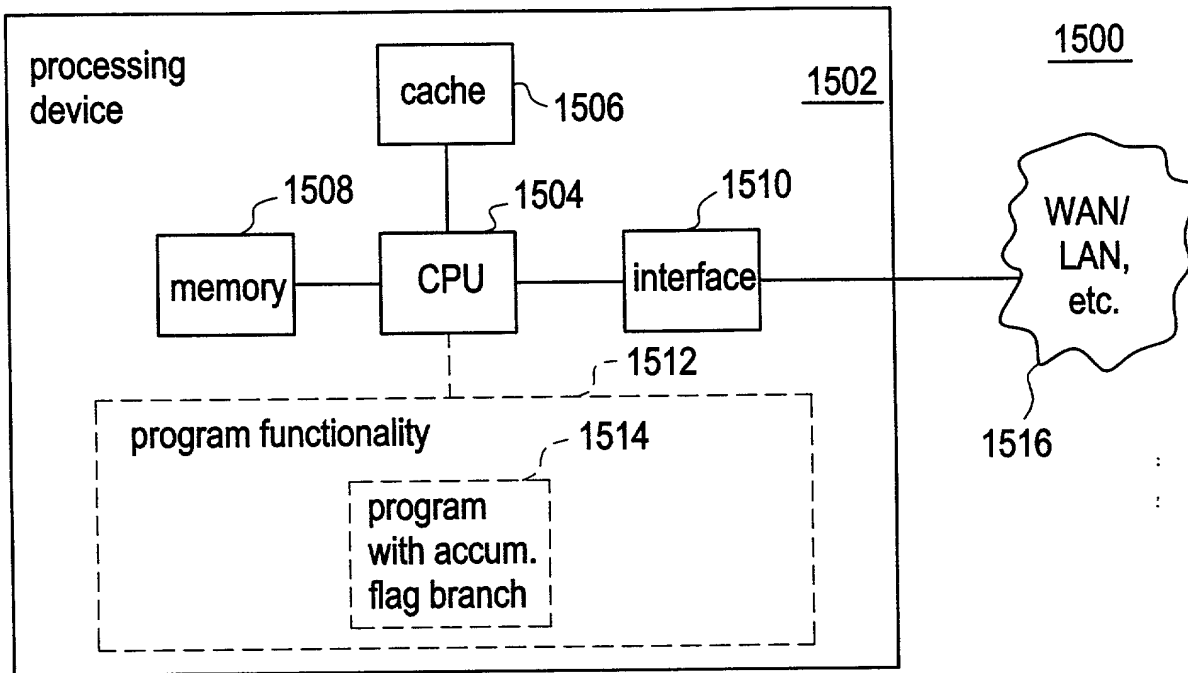


FIG. 89

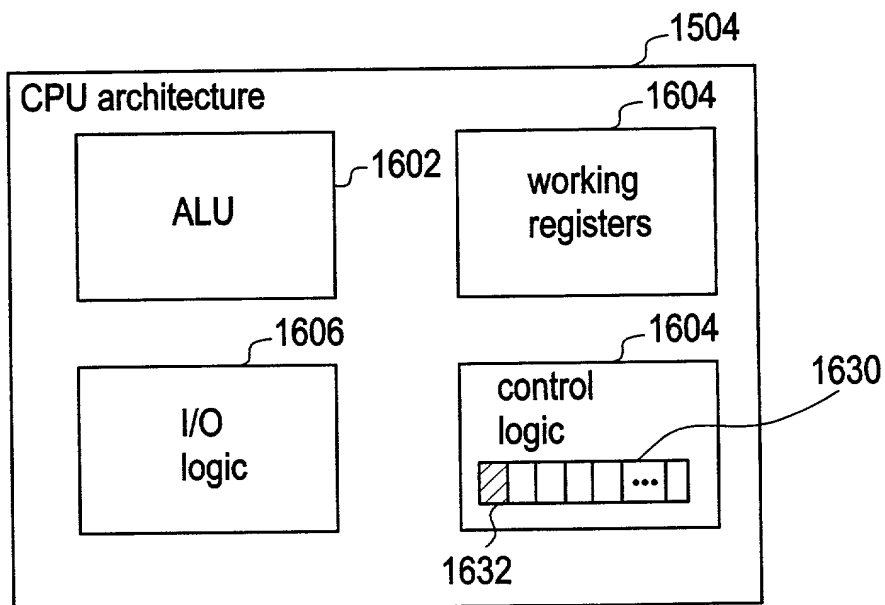


FIG. 90

